

F I G . 1

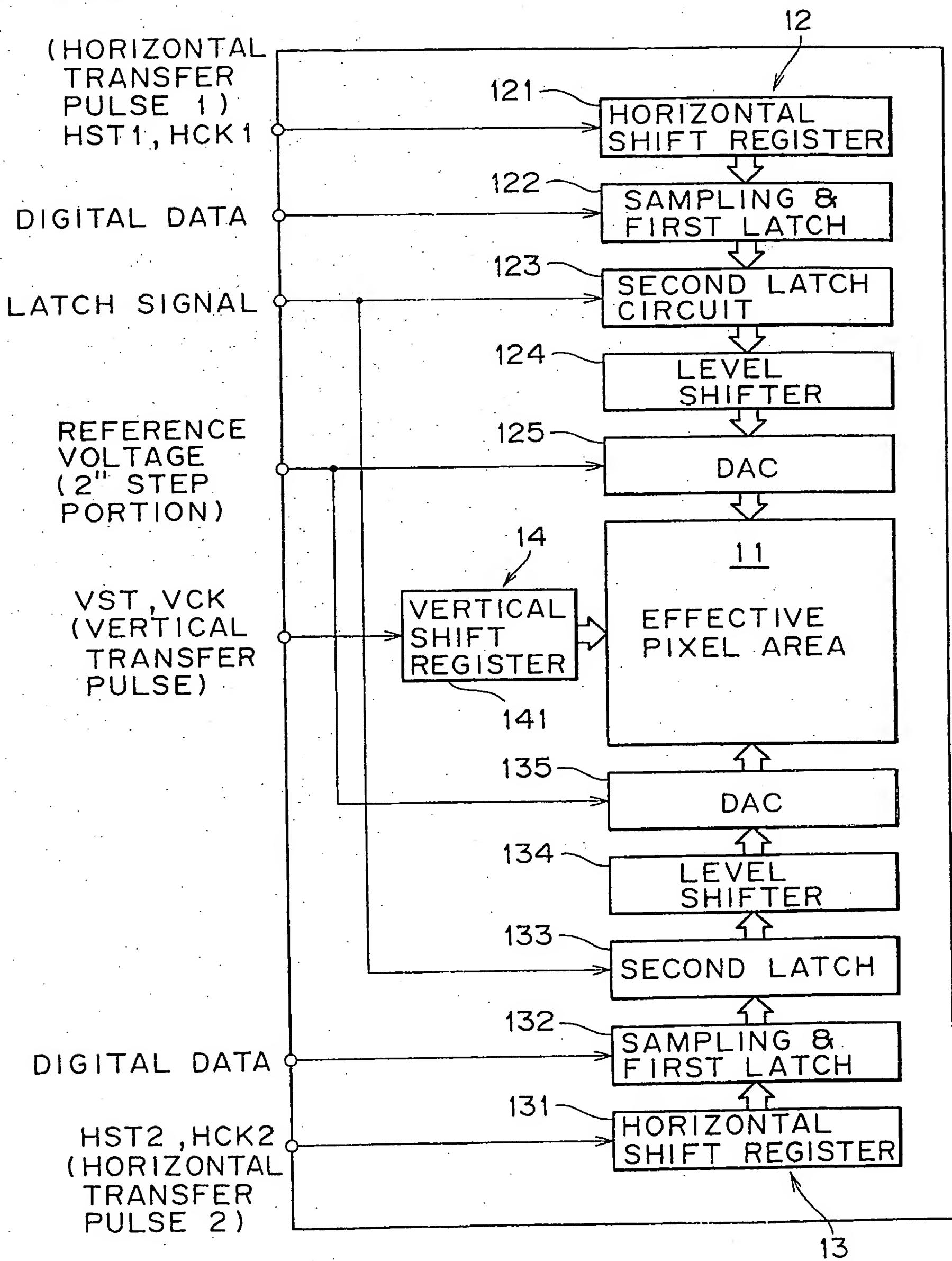


FIG. 2

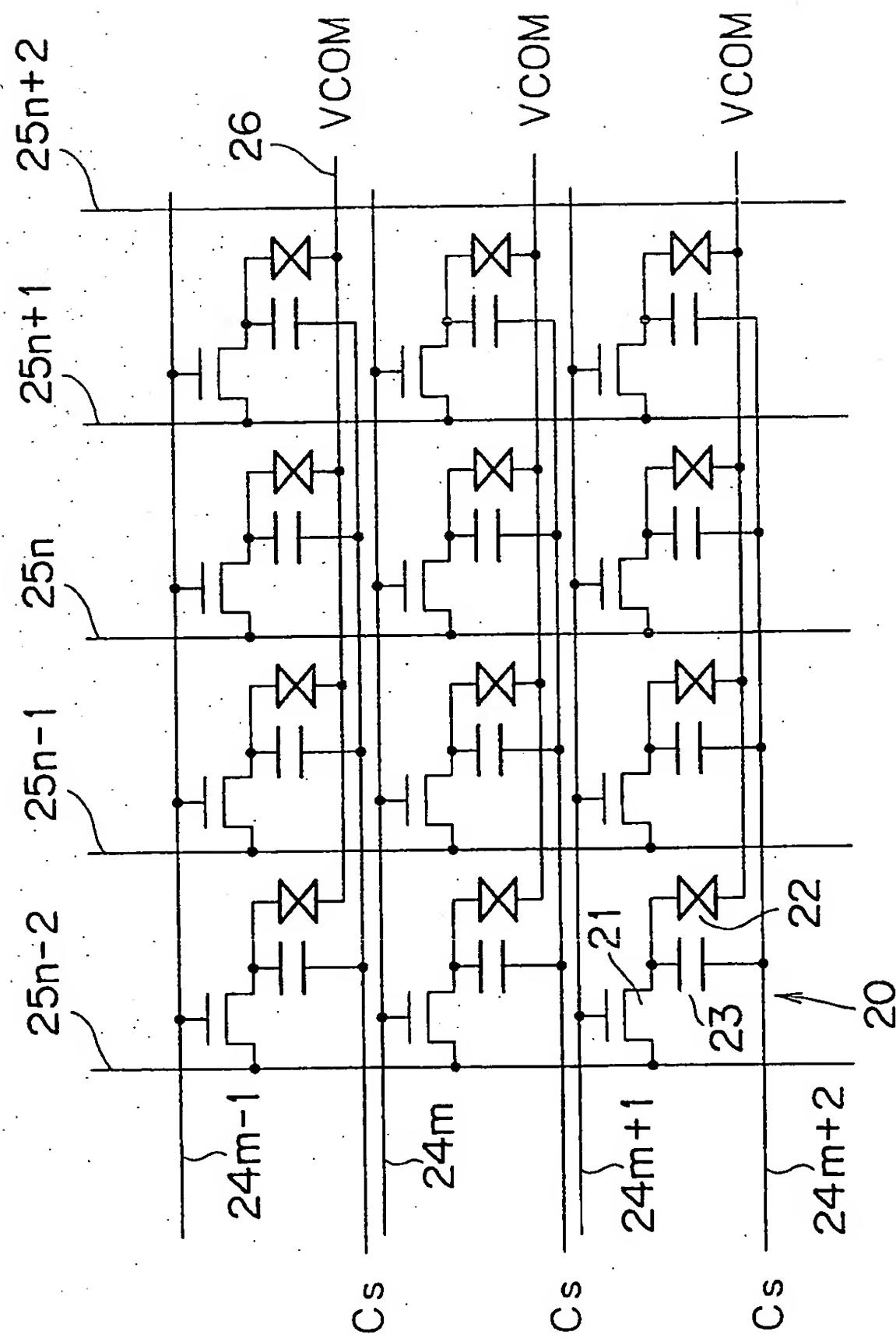
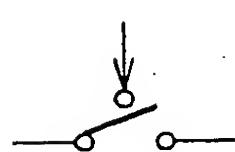
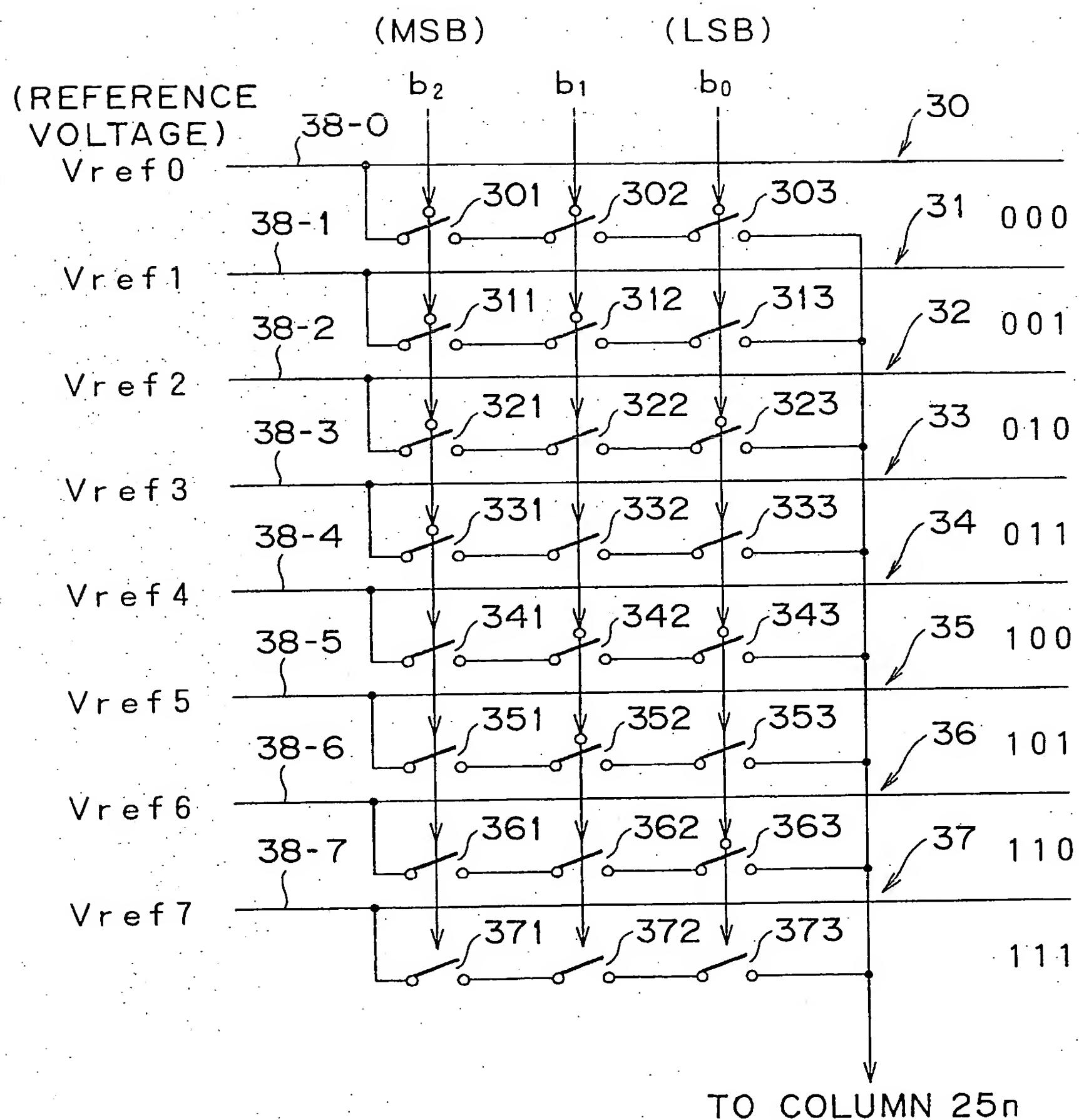
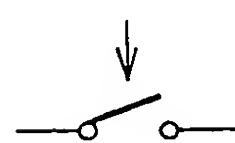


FIG. 3

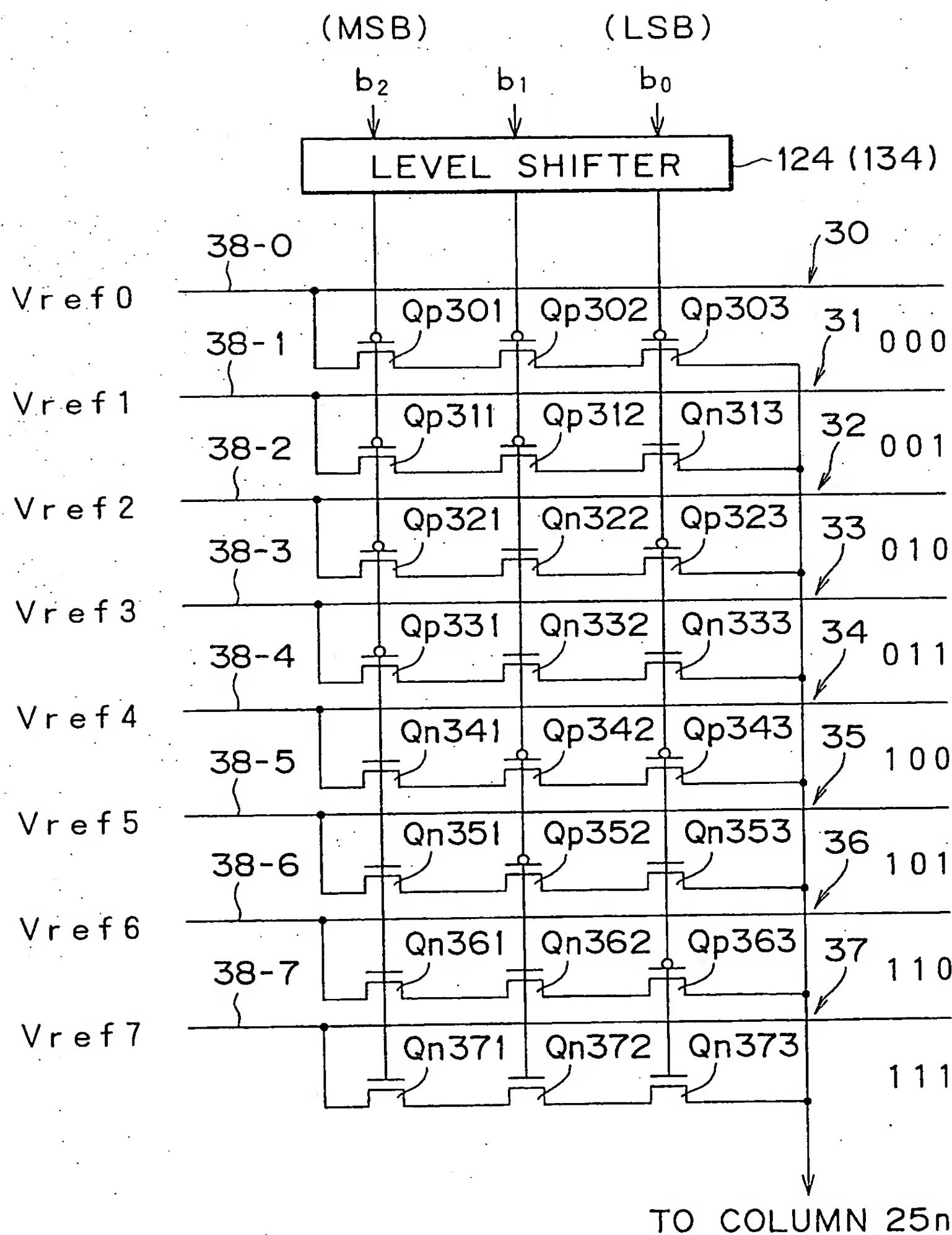


NEGATIVE POLARITY ANALOG SWITCH

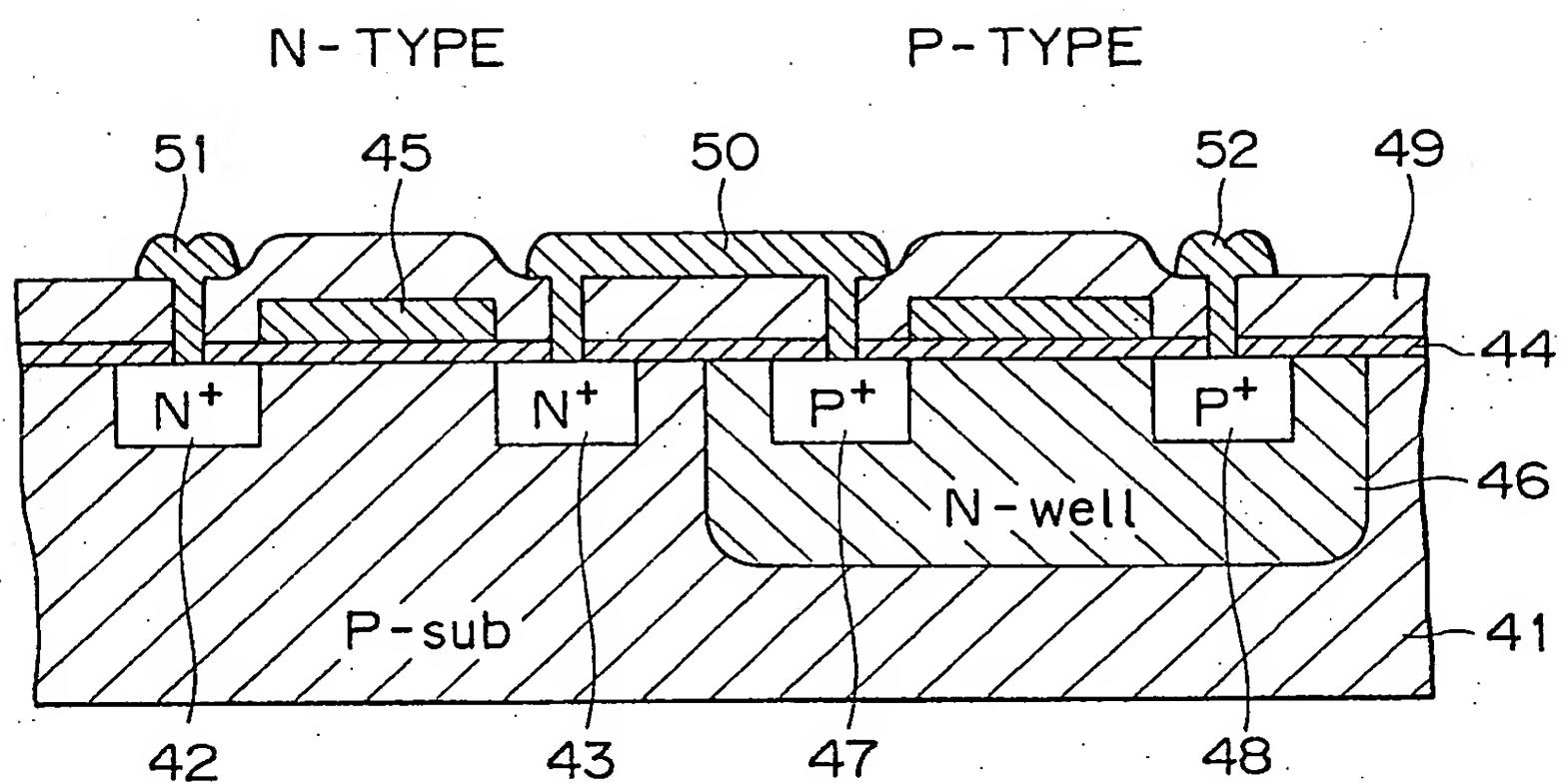


POSITIVE POLARITY ANALOG SWITCH

FIG. 4



F I G. 5



F I G. 6

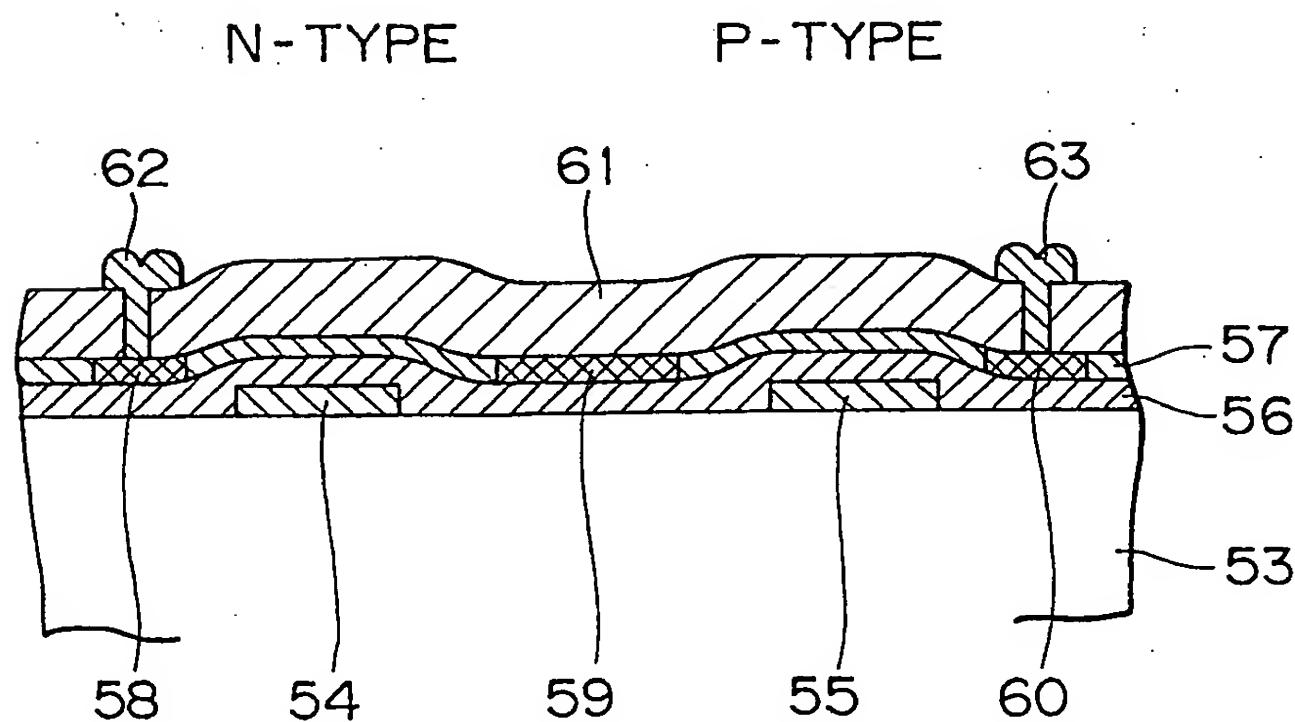


FIG. 7

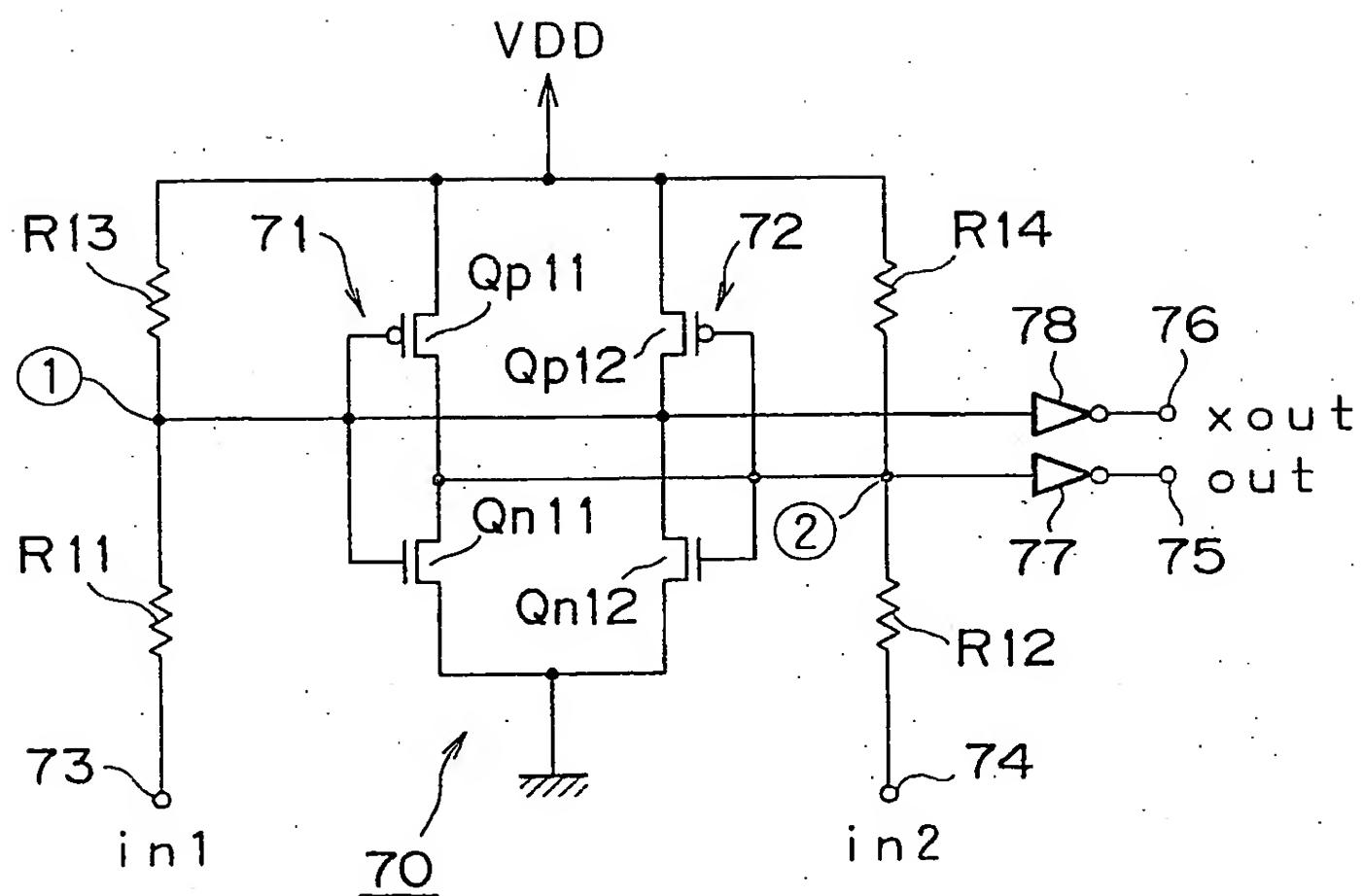
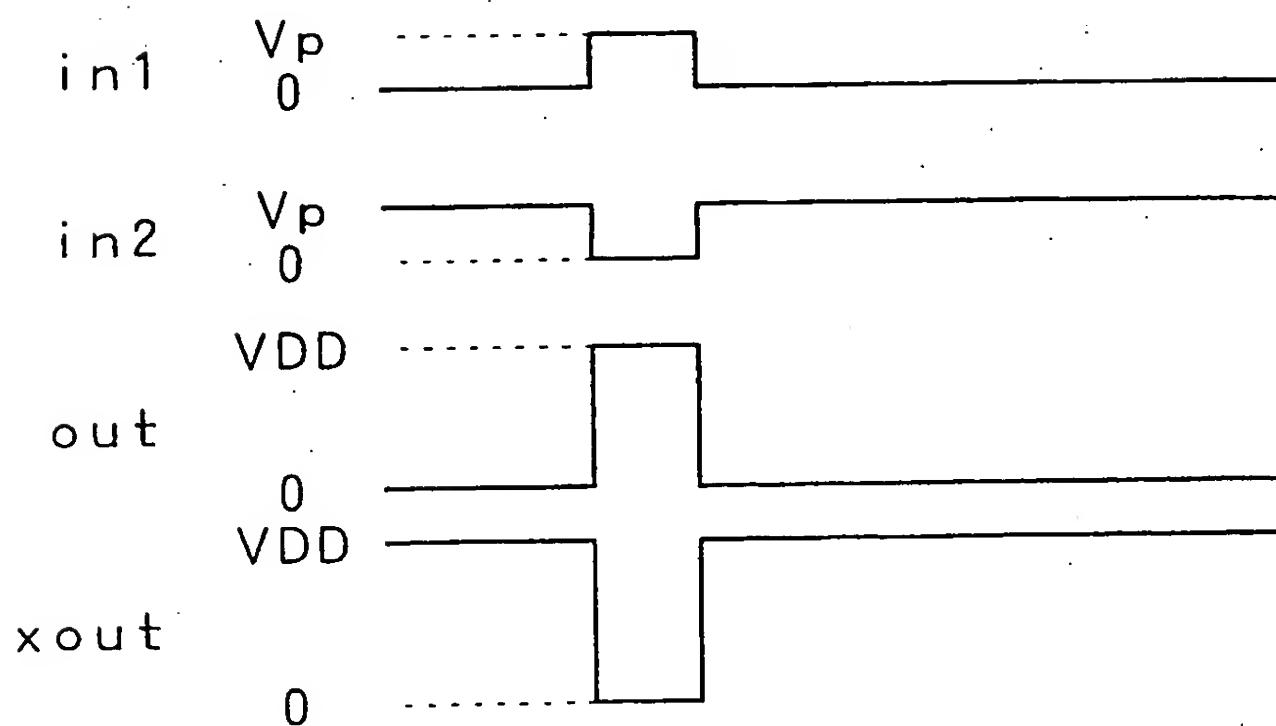
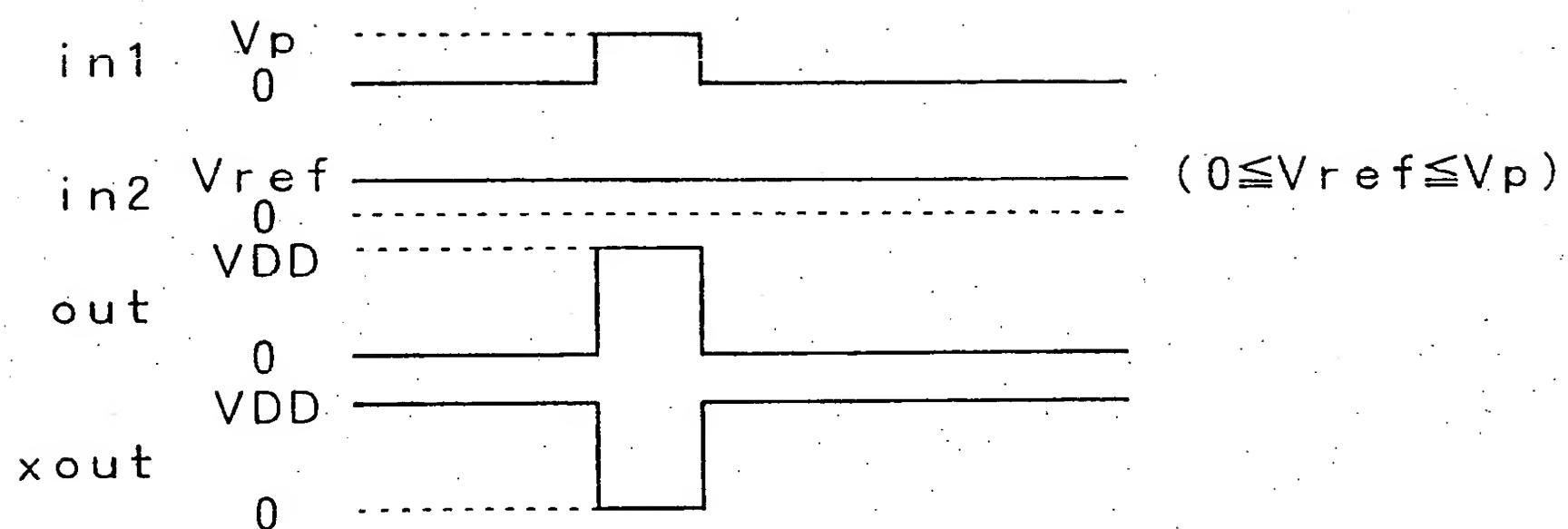


FIG. 8



F I G. 9



F I G. 10

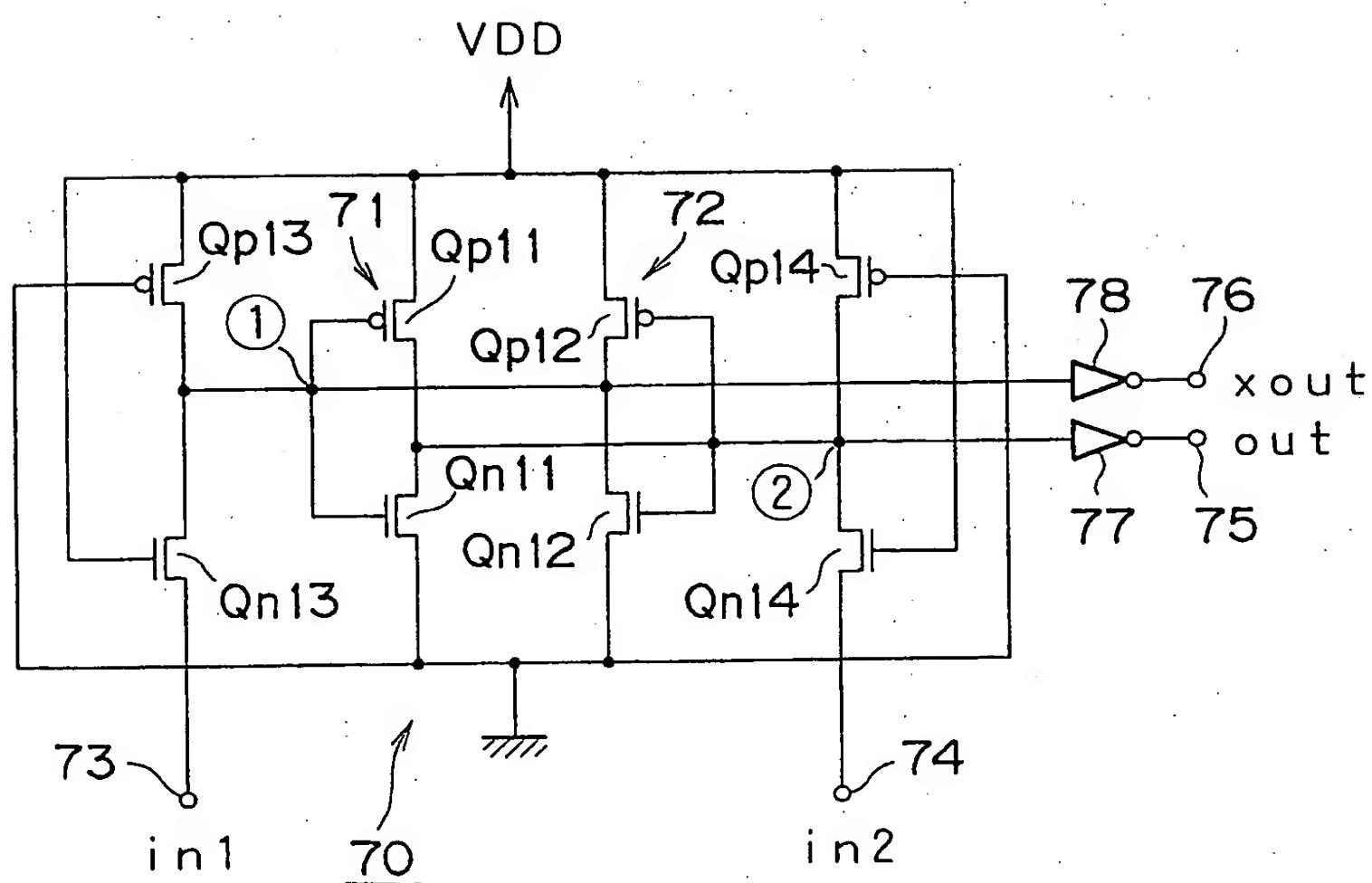


FIG. 10B
RELATED ART

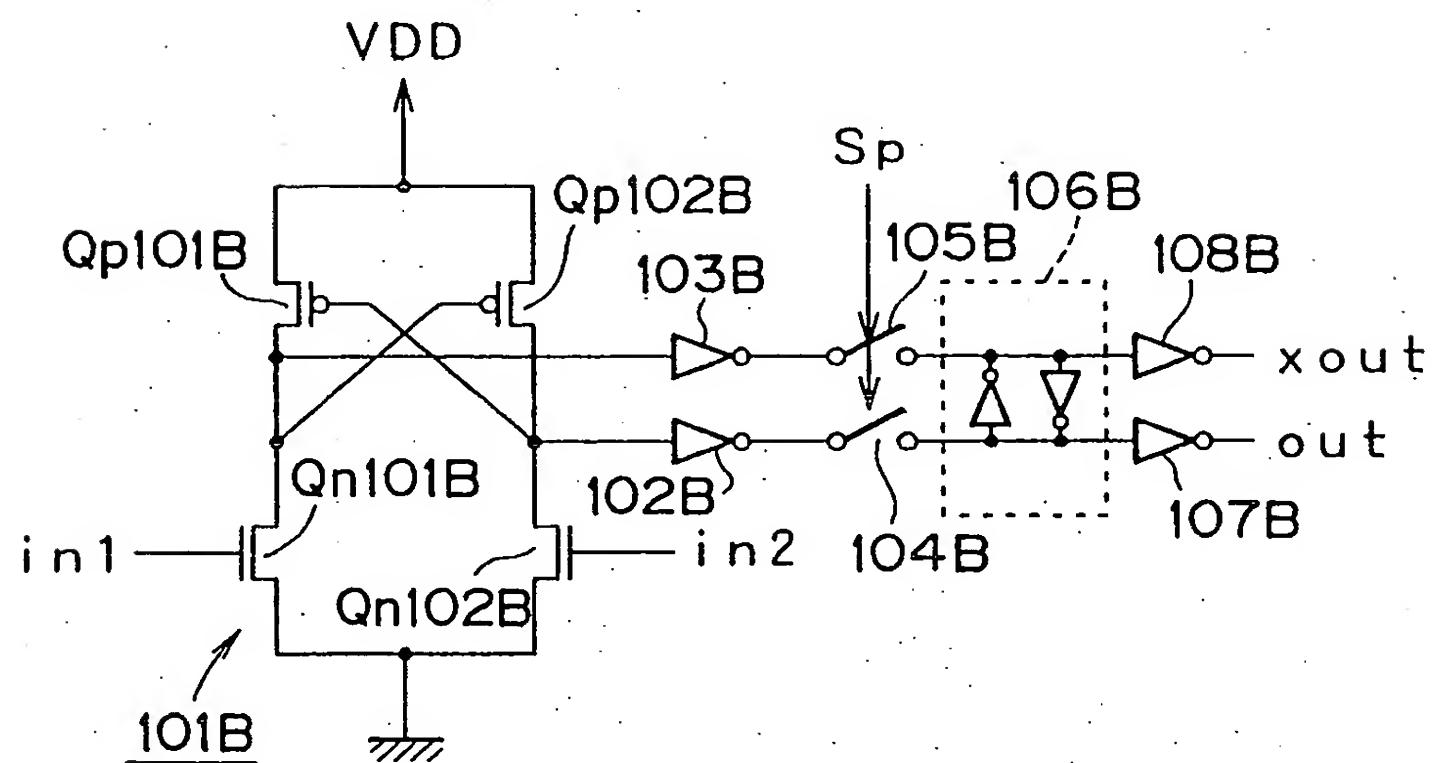


FIG. 10C
RELATED ART 1

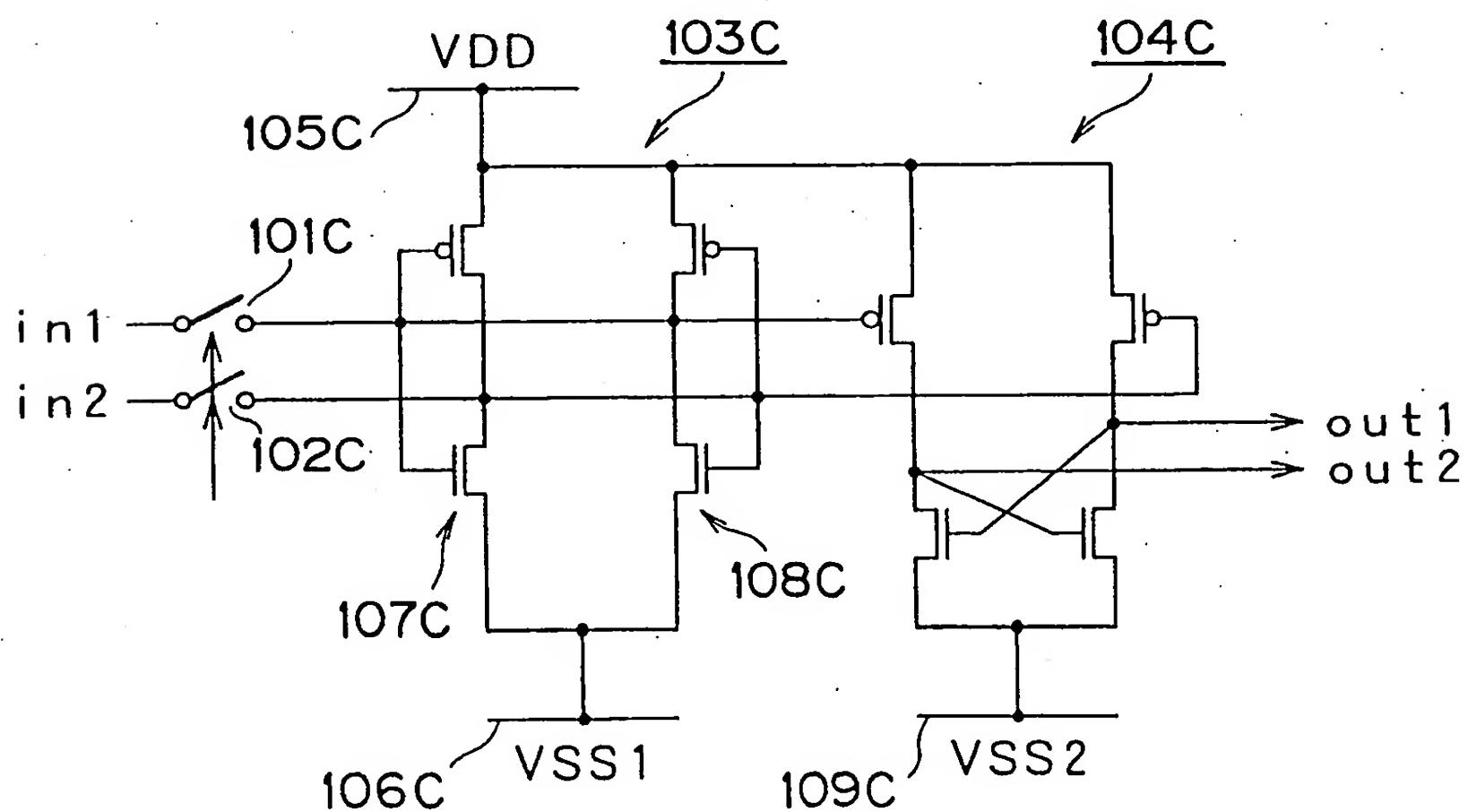


FIG. 11

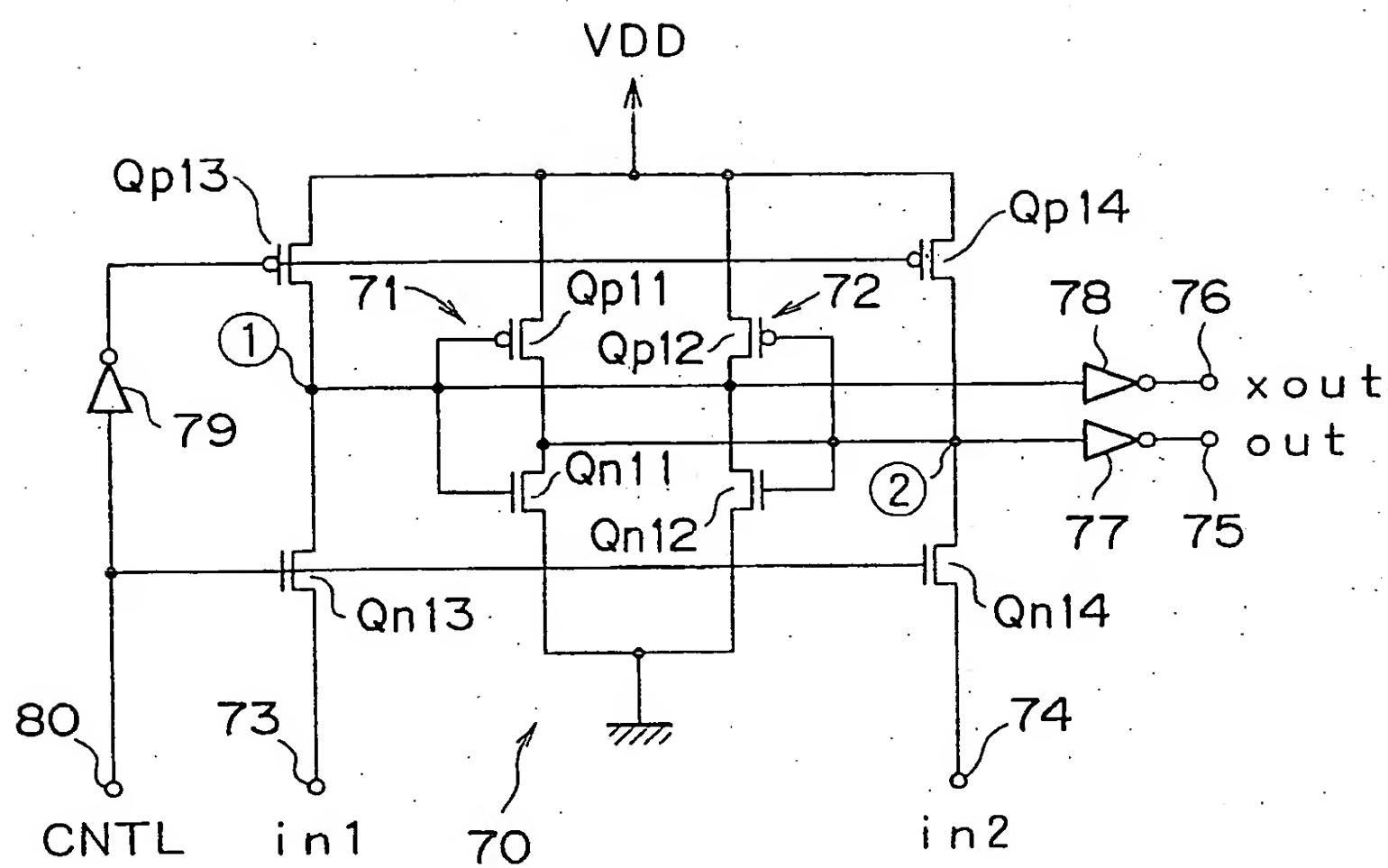
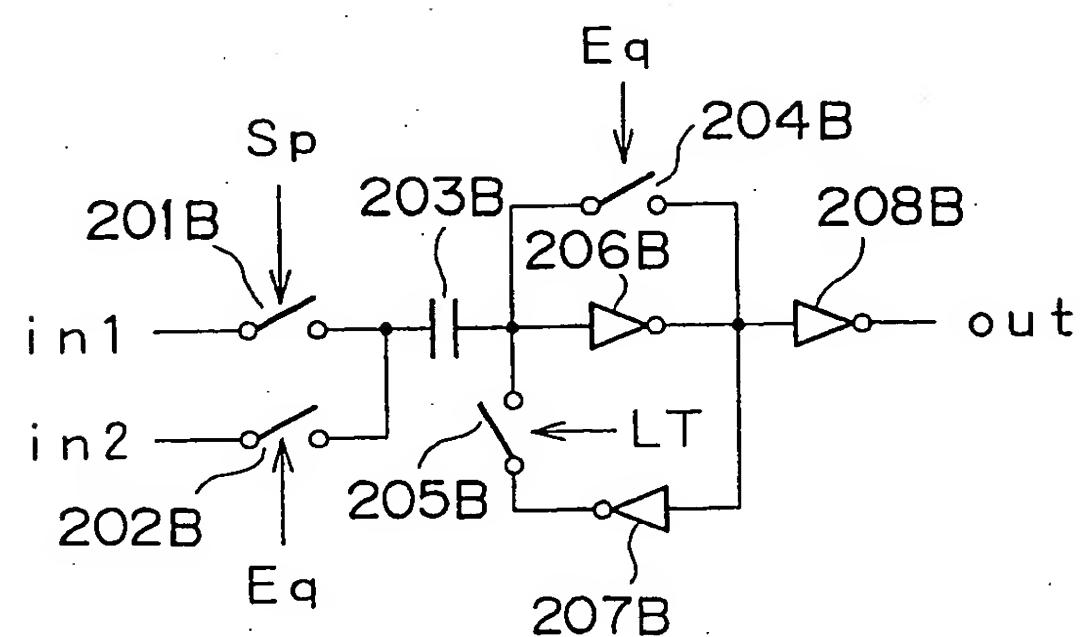
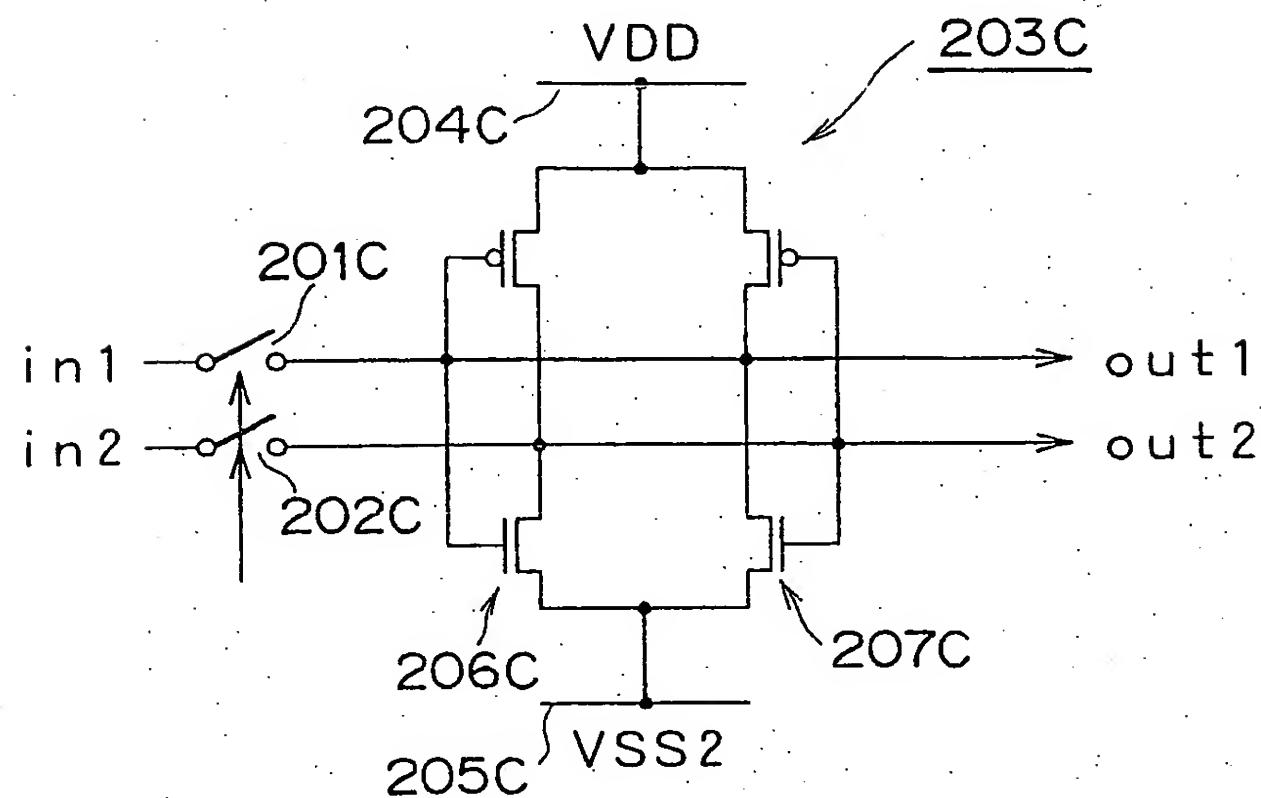


FIG. 11B
RELATED ART



F I G. 11C
RELATED ART 2



F I G. 12

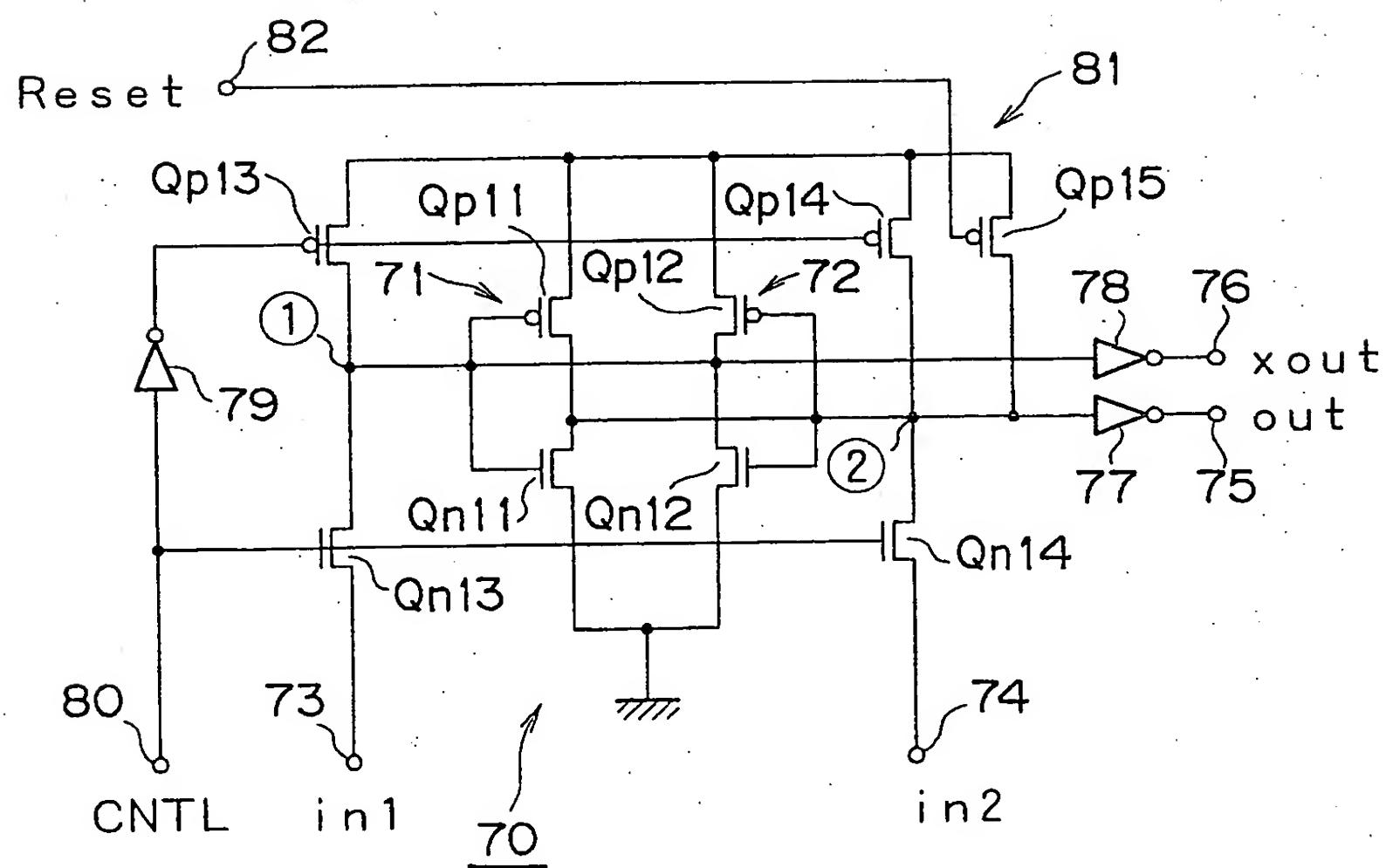


FIG. 12C
RELATED ART 3

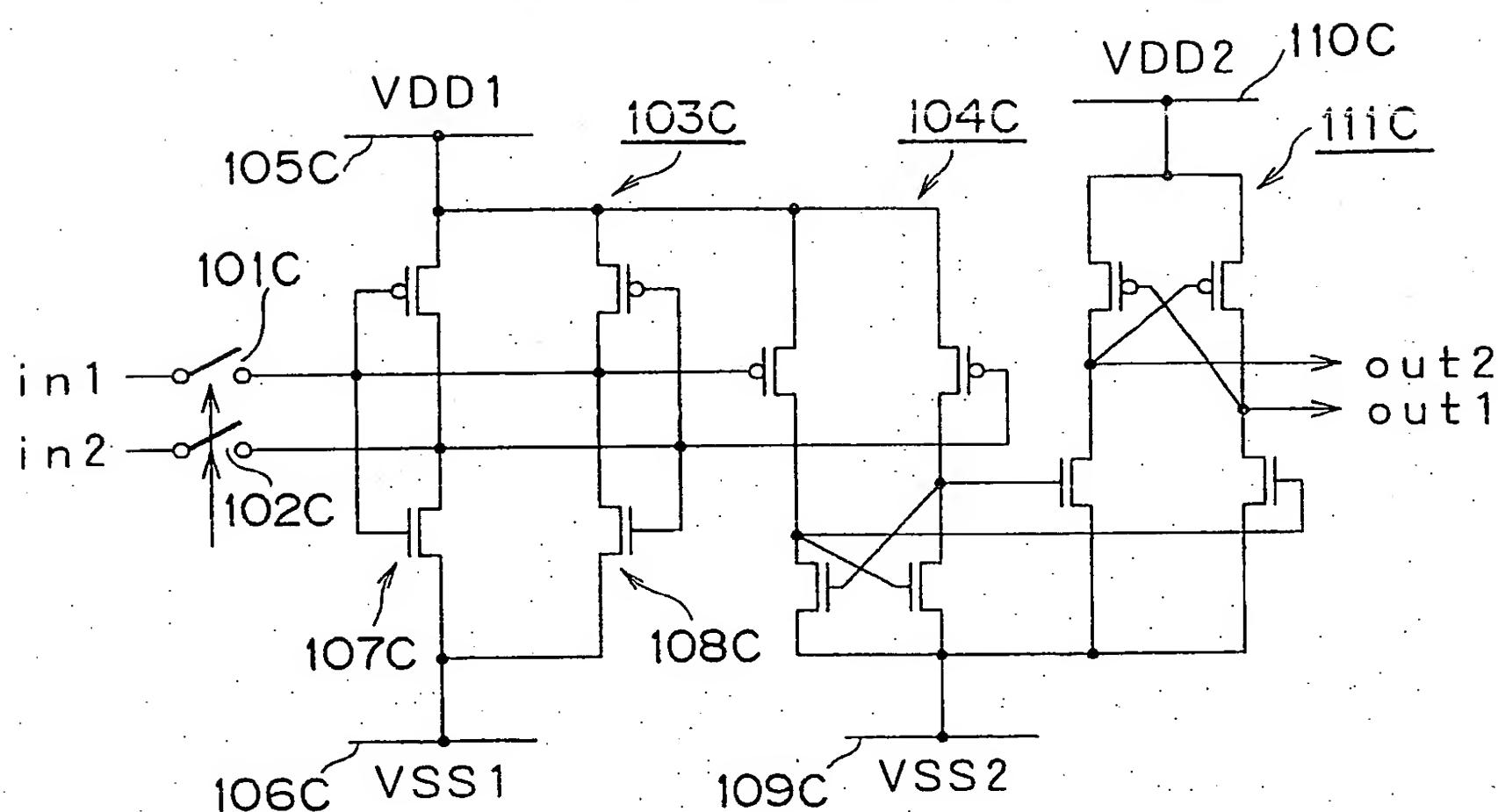
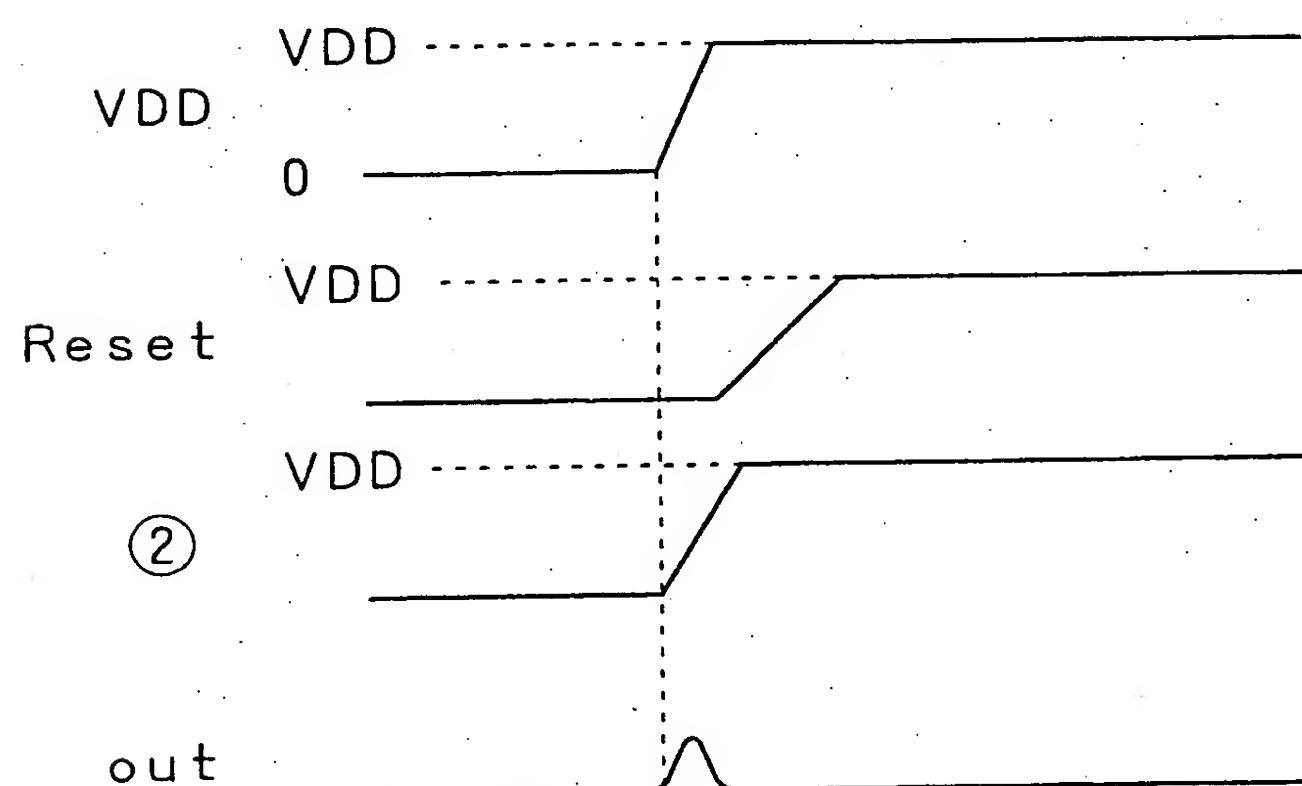
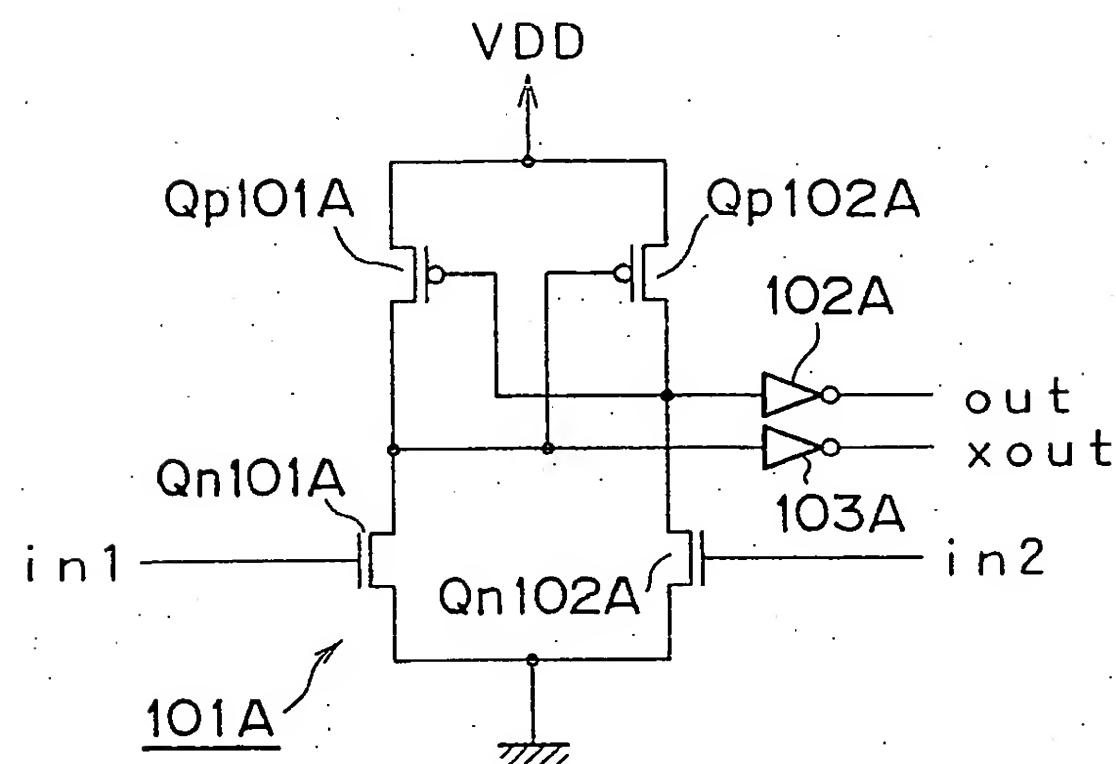


FIG. 13



F I G. 13A
RELATED ART 1



F I G. 13C
RELATED ART 4

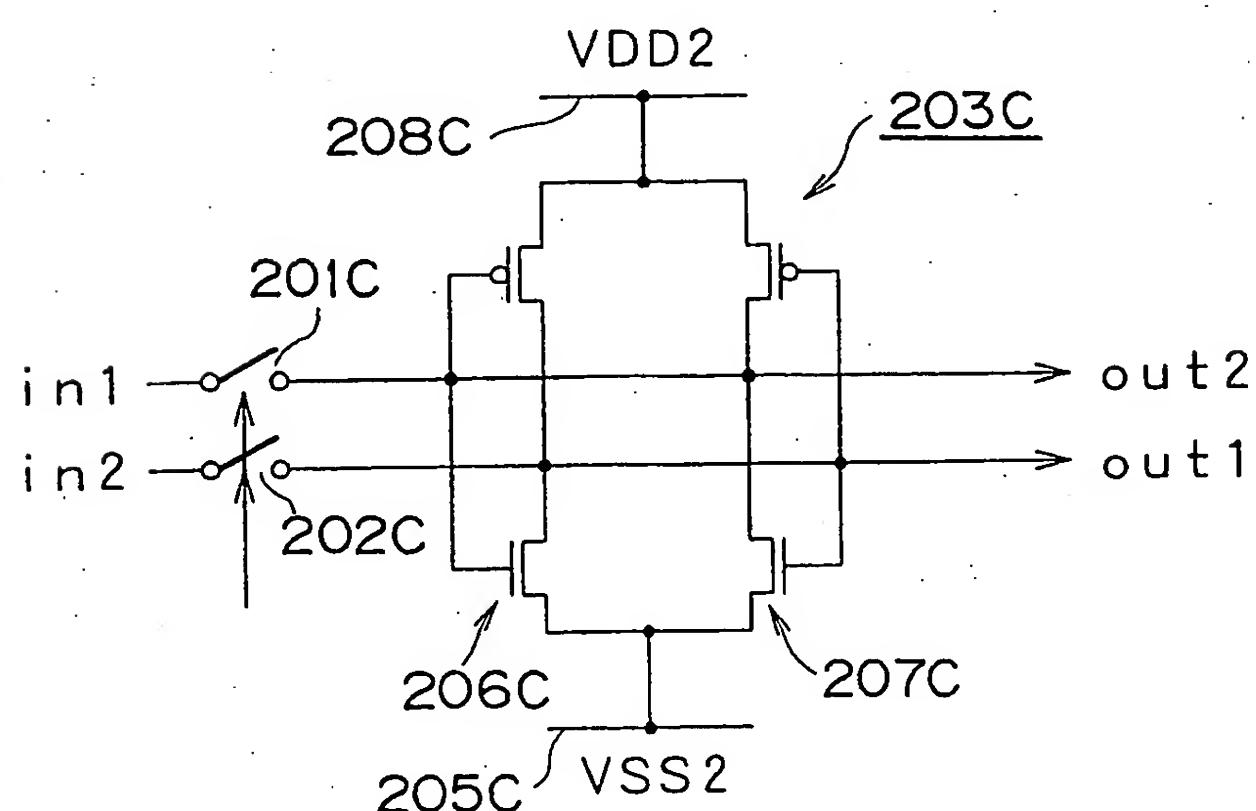


FIG. 14

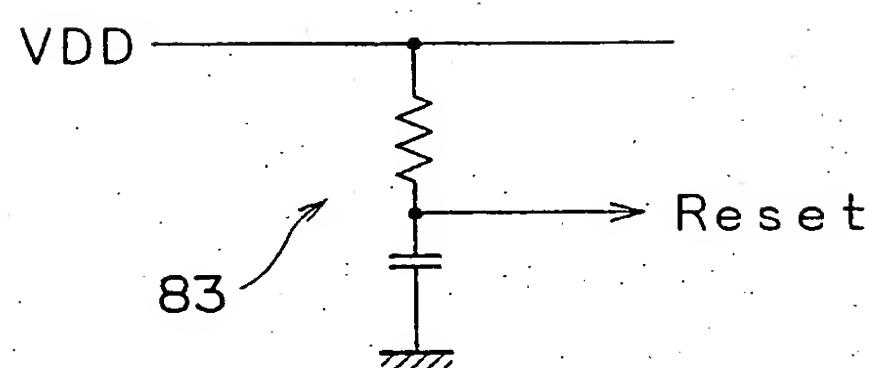


FIG. 14A
RELATED ART 2

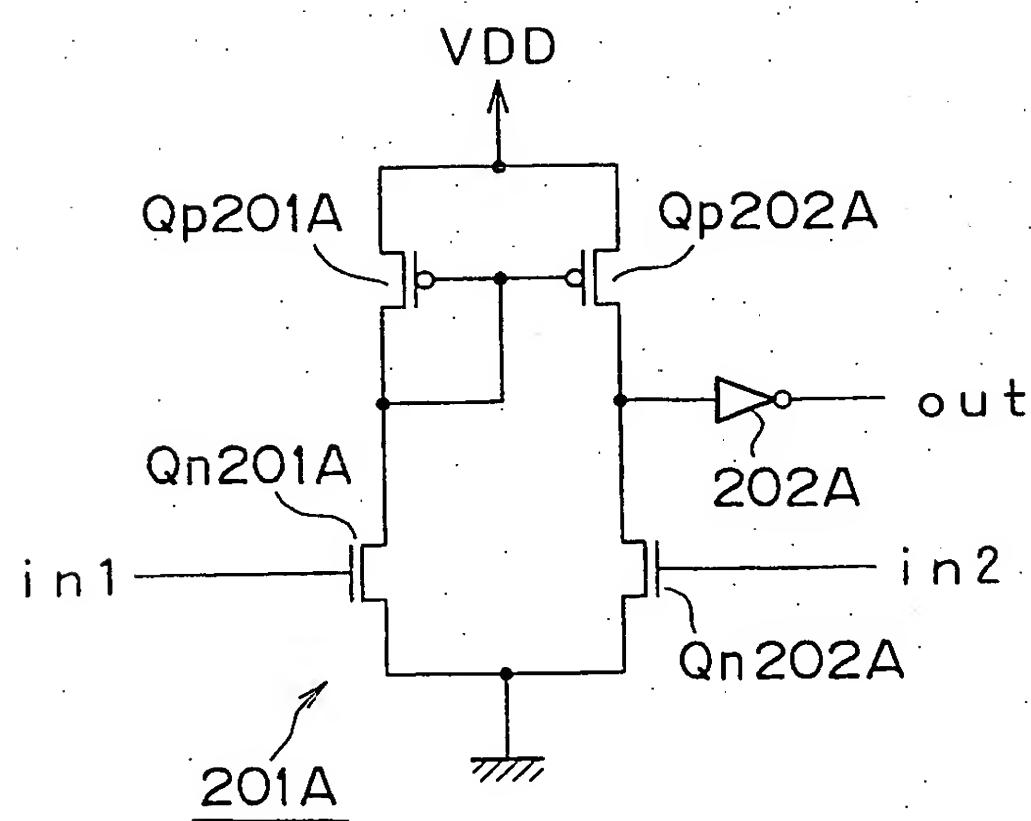


FIG. 15

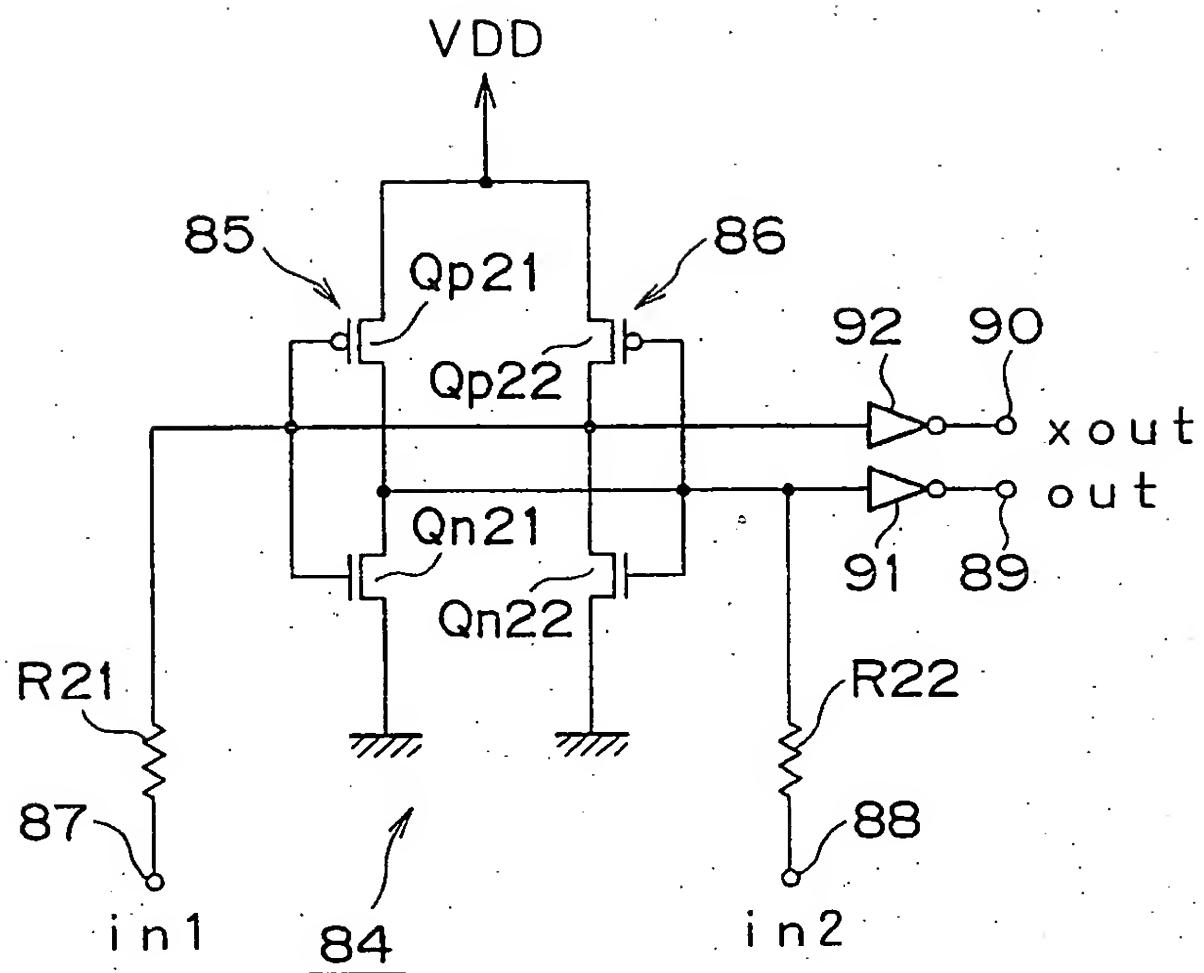


FIG. 15A
RELATED ART 3

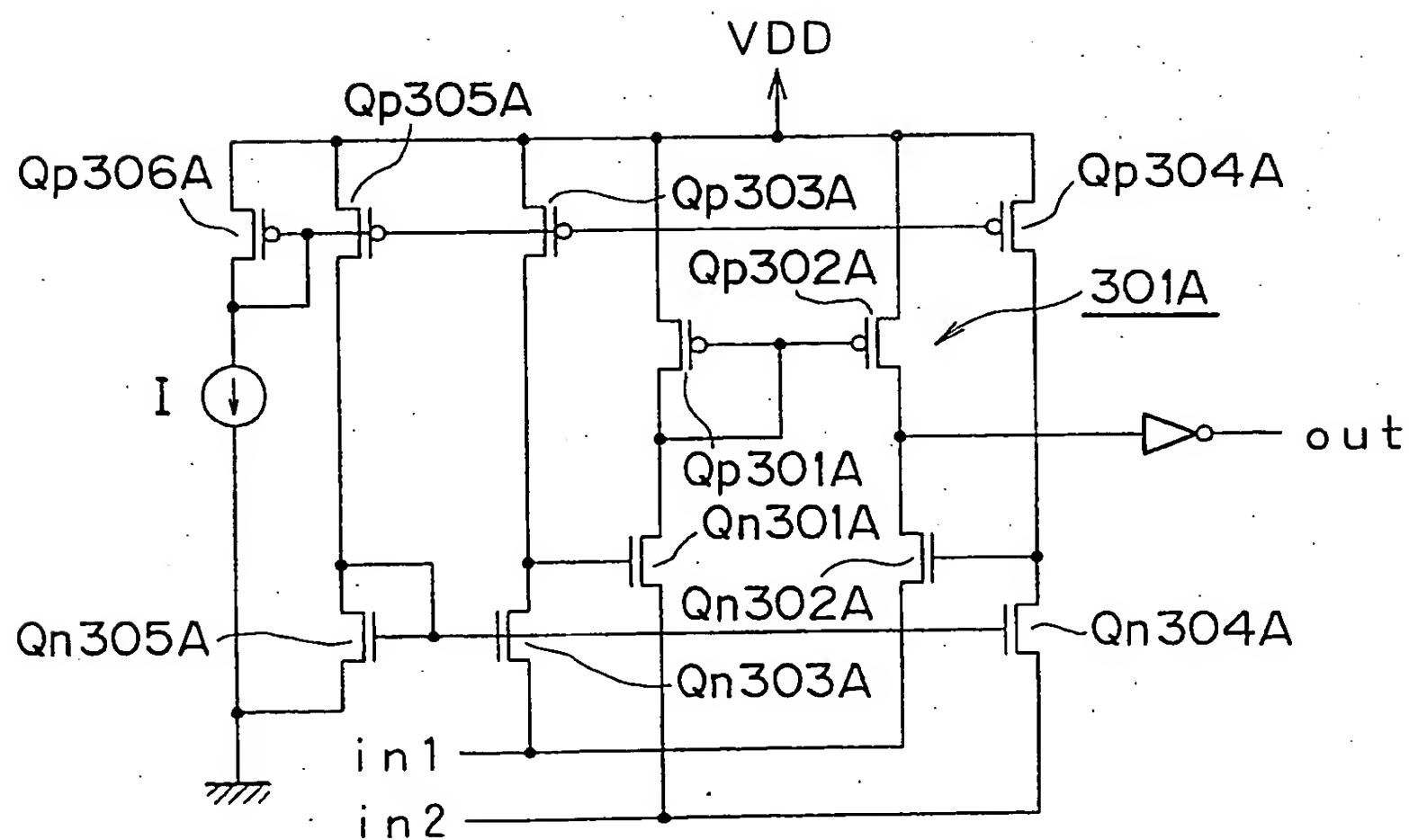
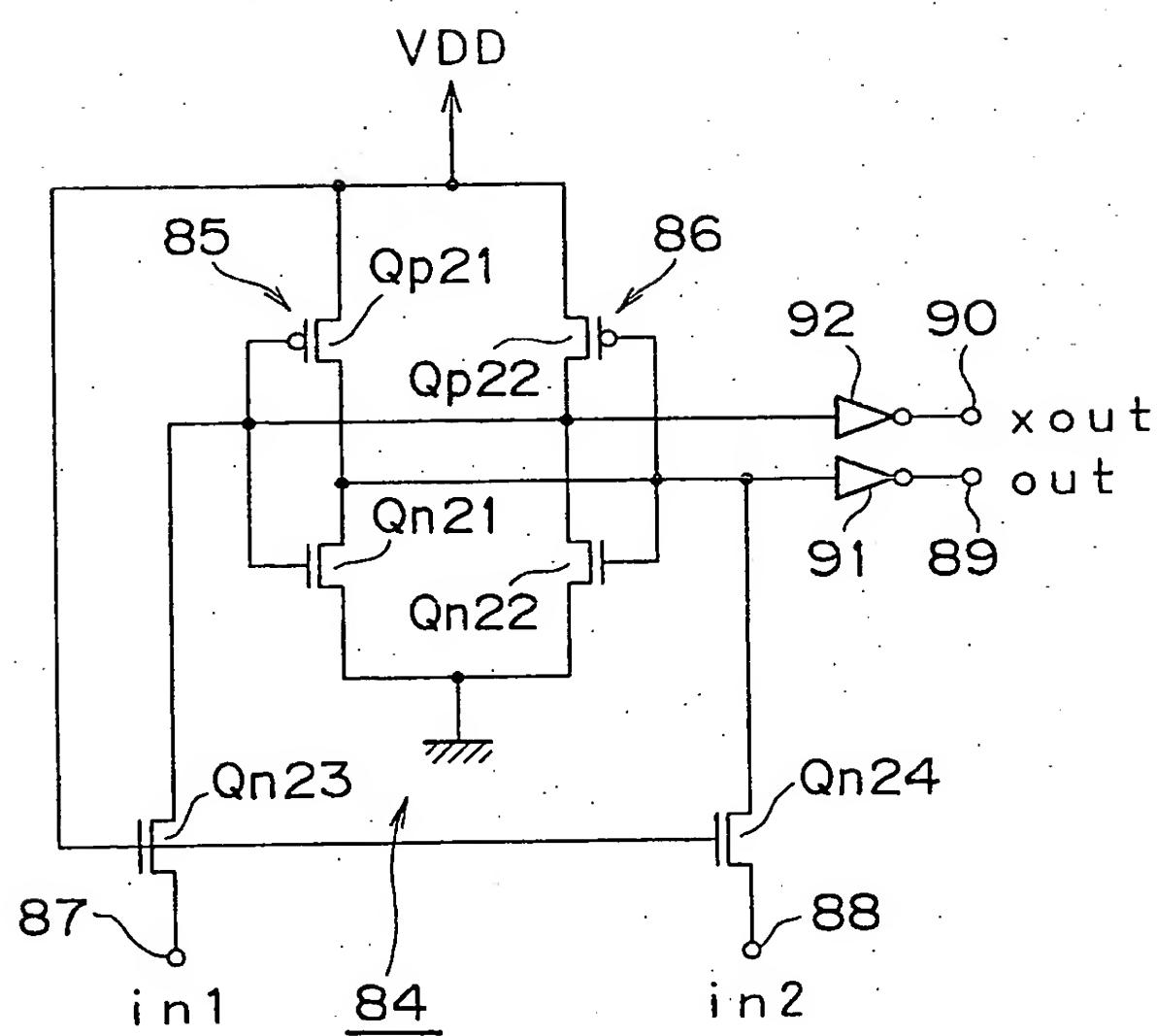
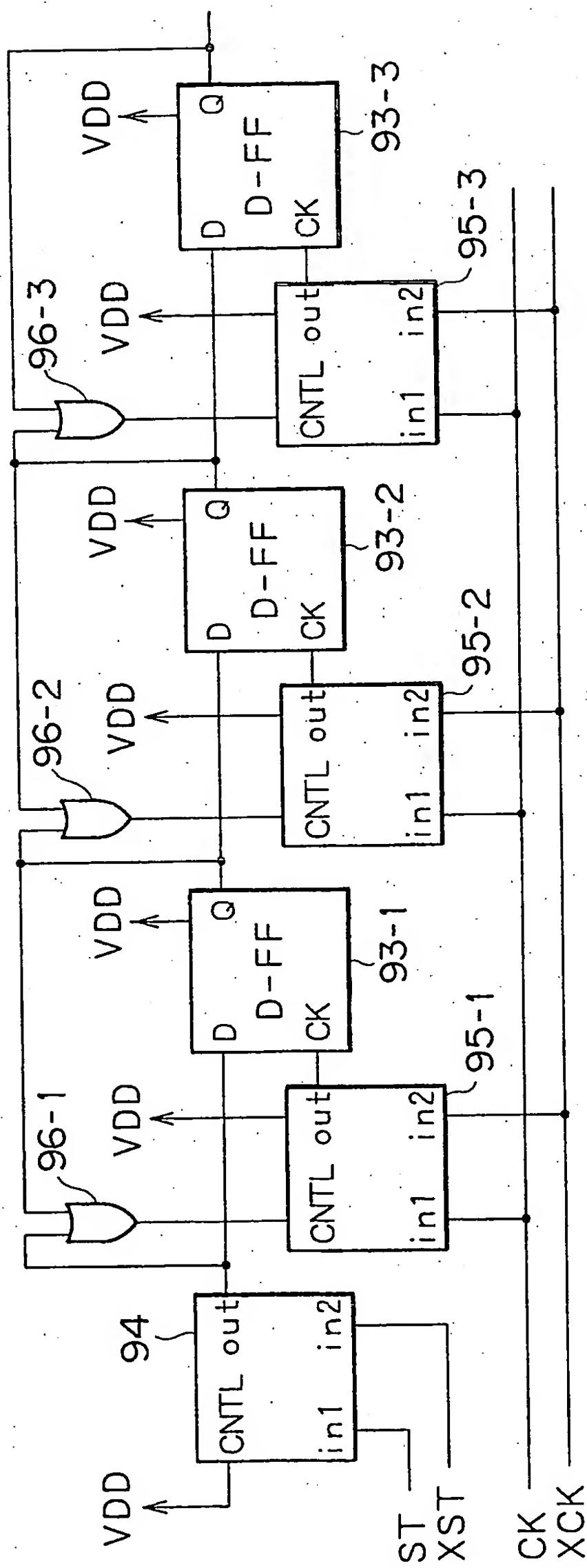


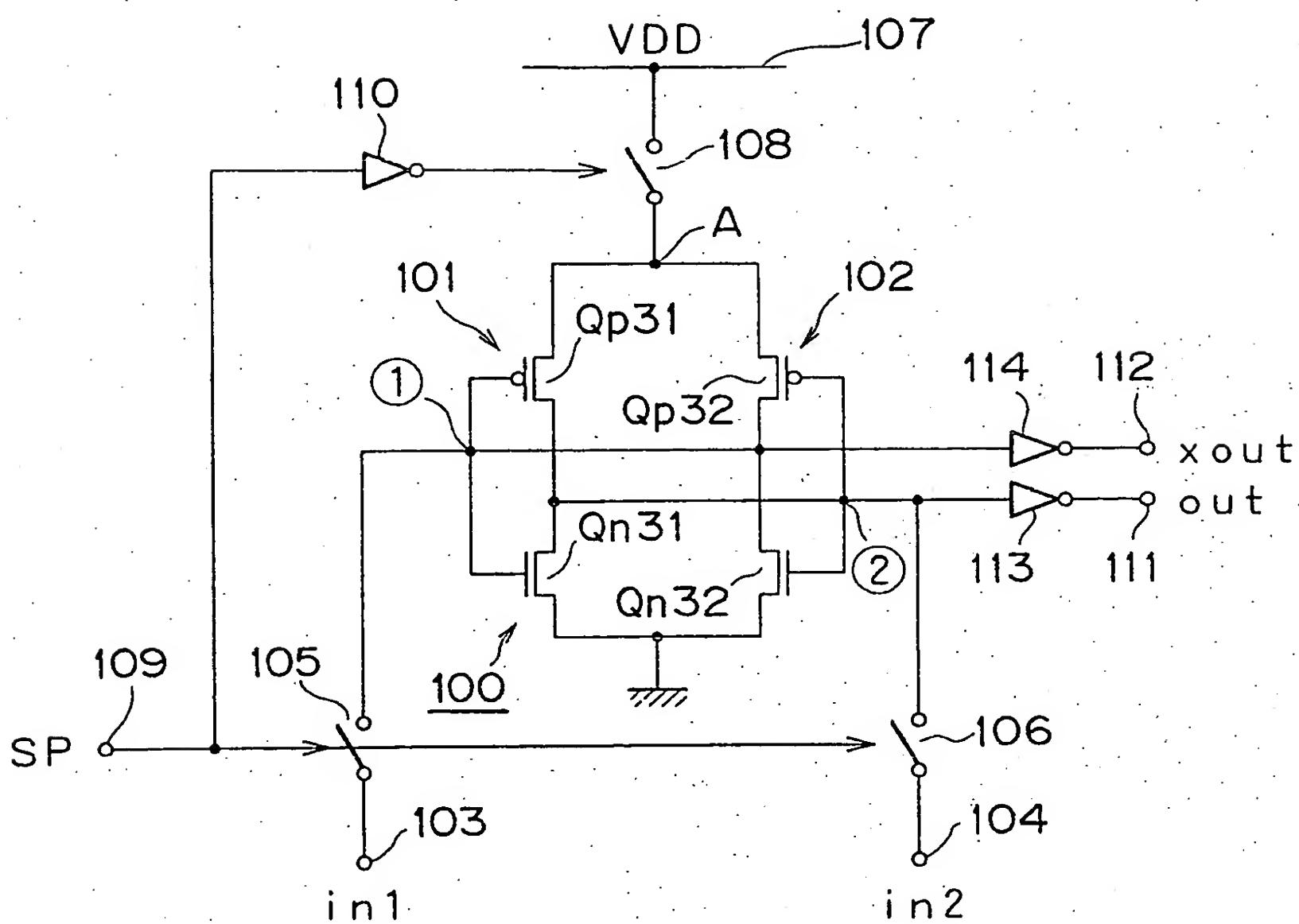
FIG. 16



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F I G. 18



F I G. 19

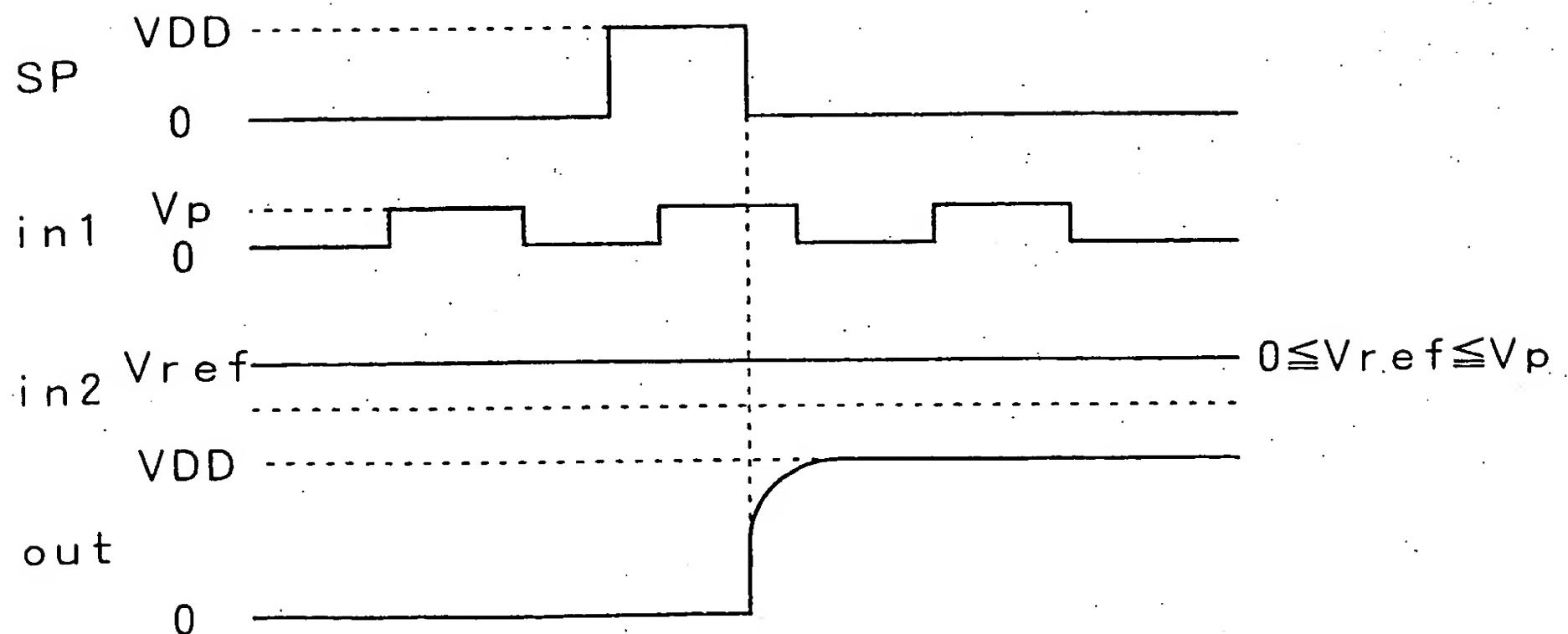


FIG. 20

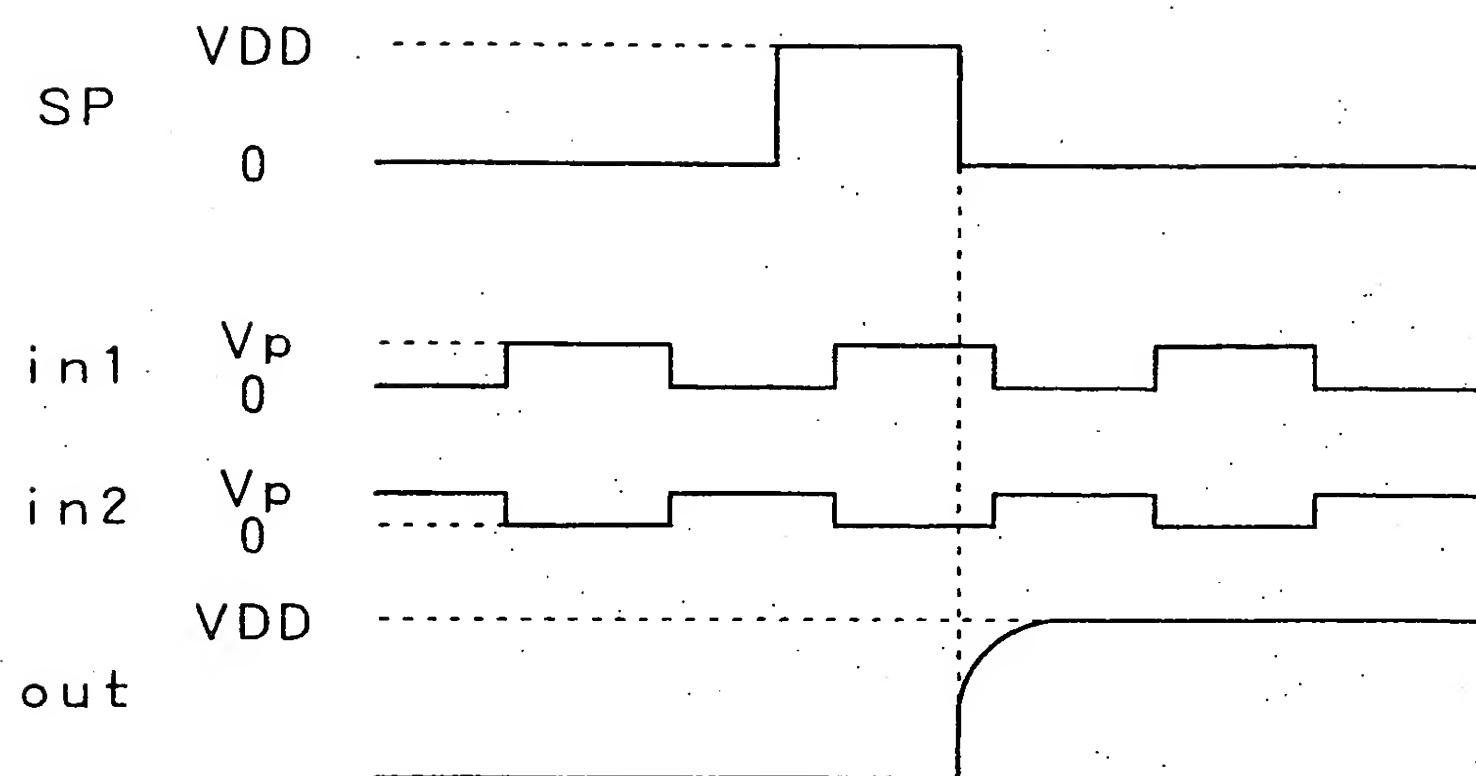


FIG. 21

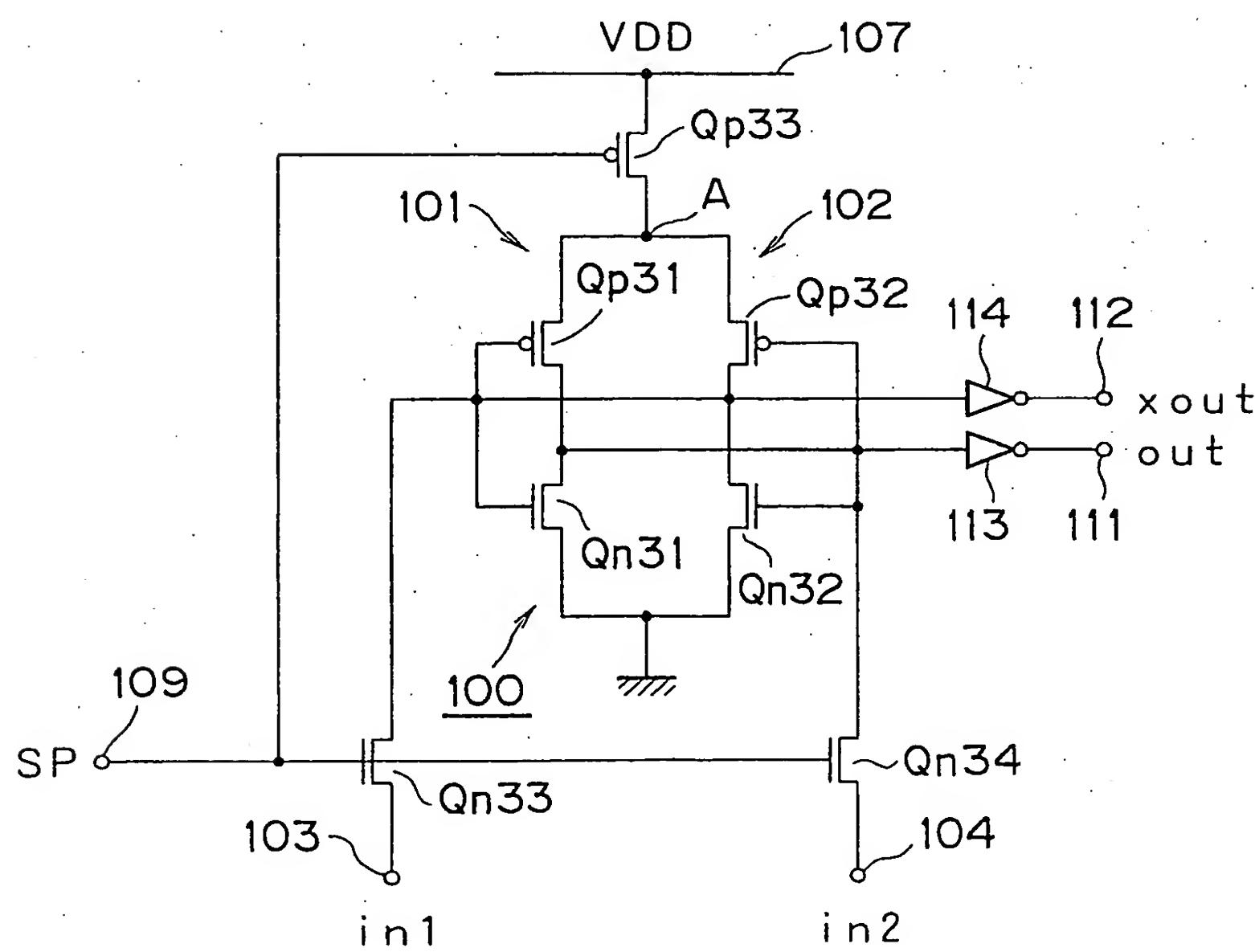


FIG. 22

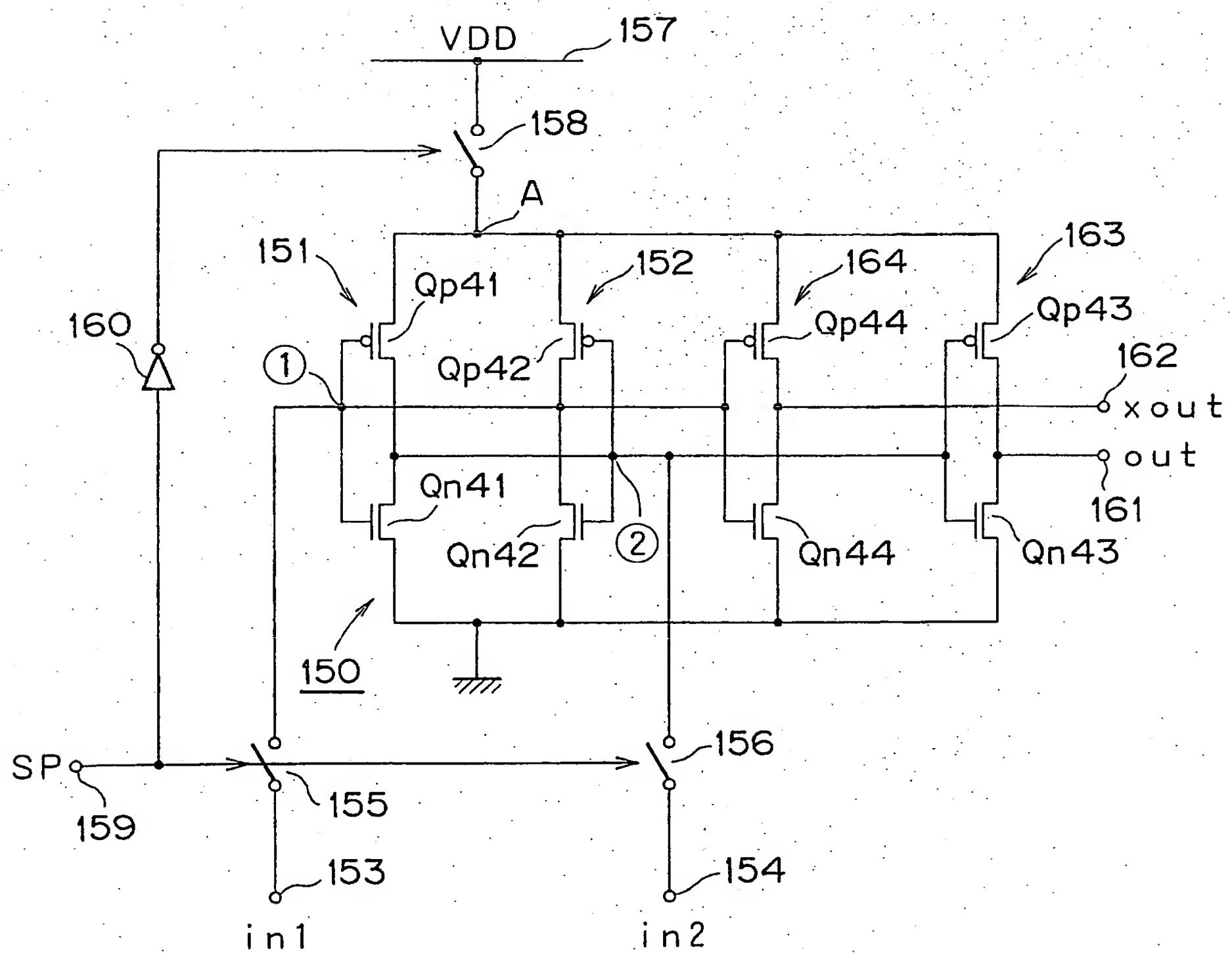


FIG. 23

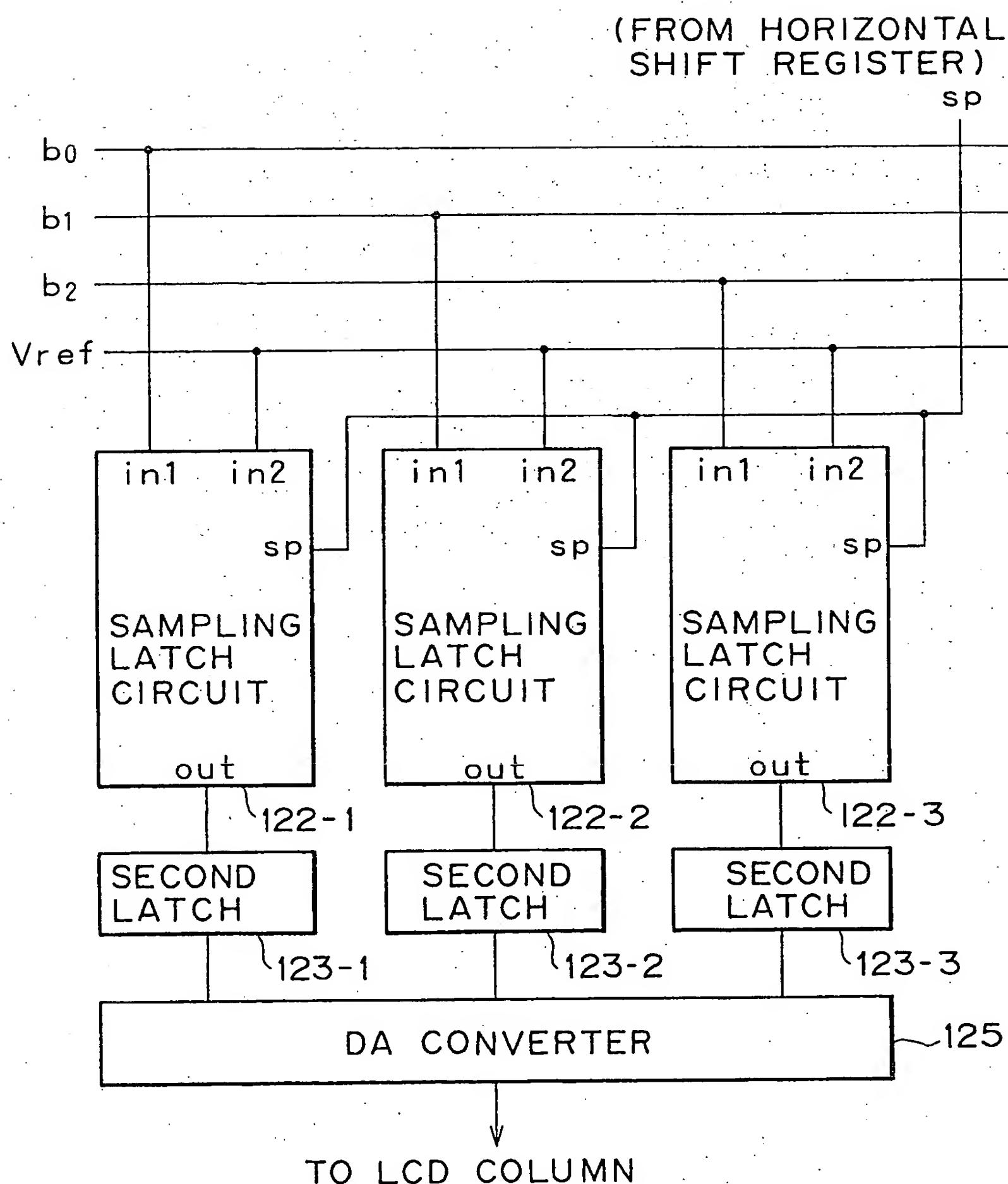


FIG. 24

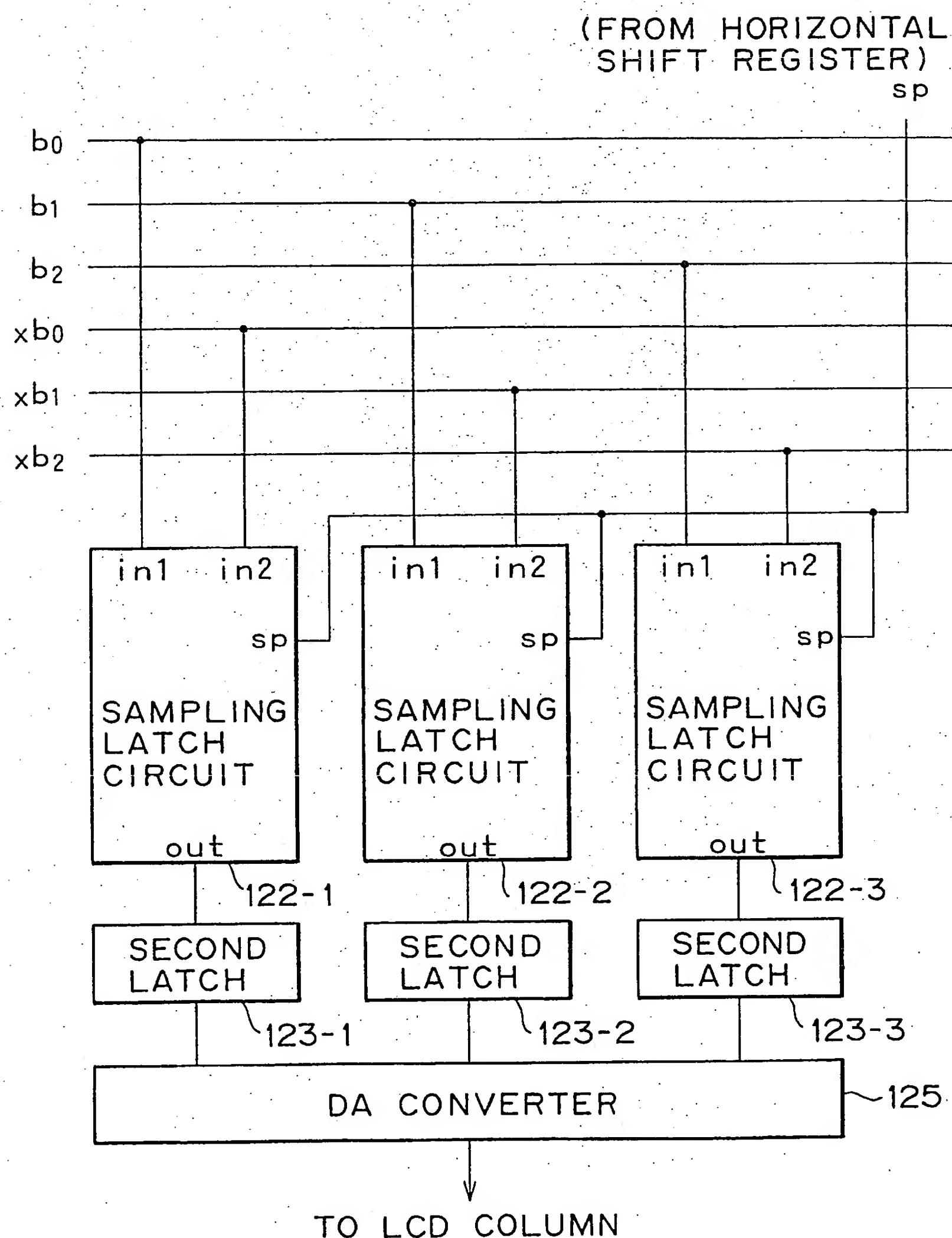


FIG. 25

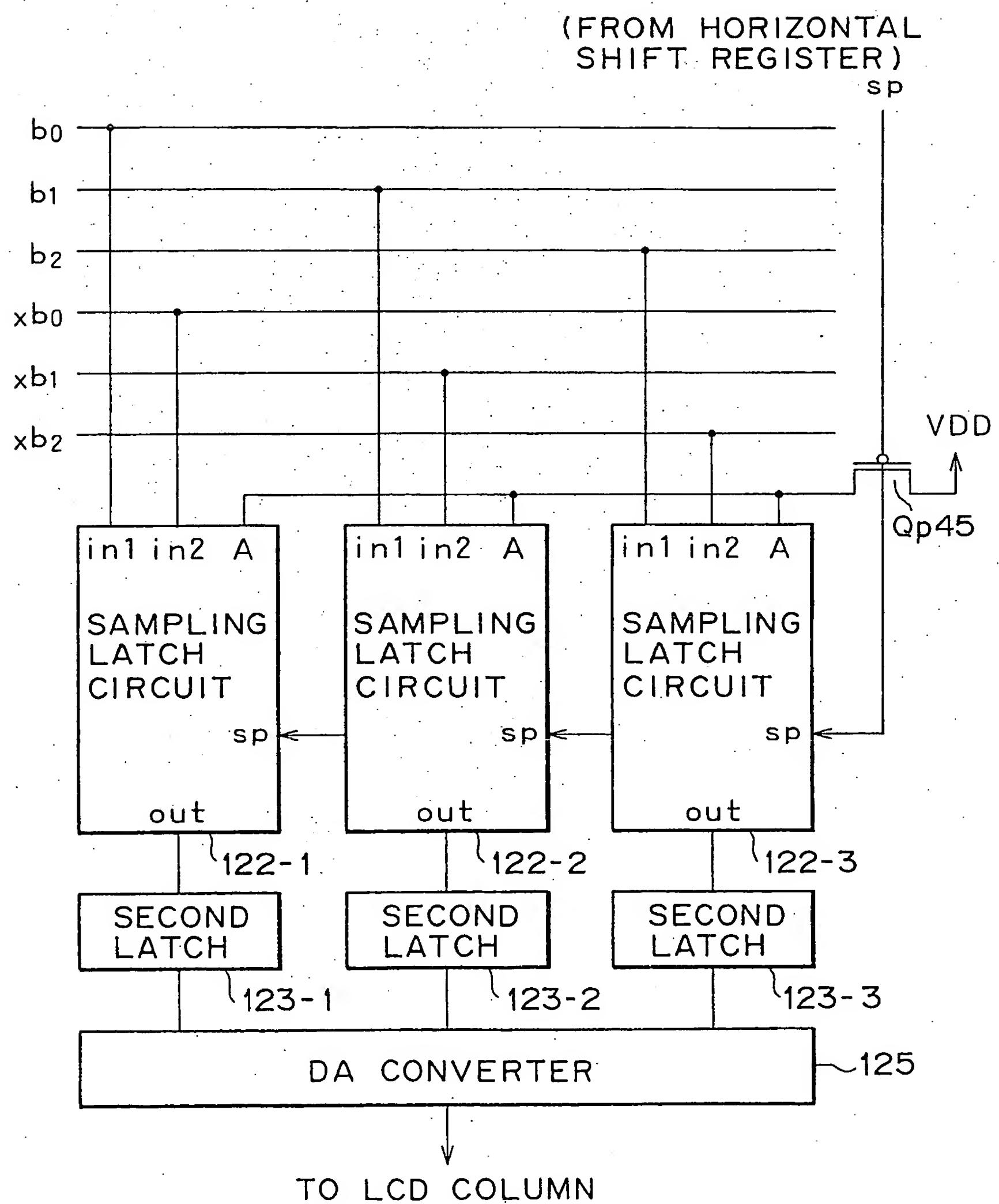
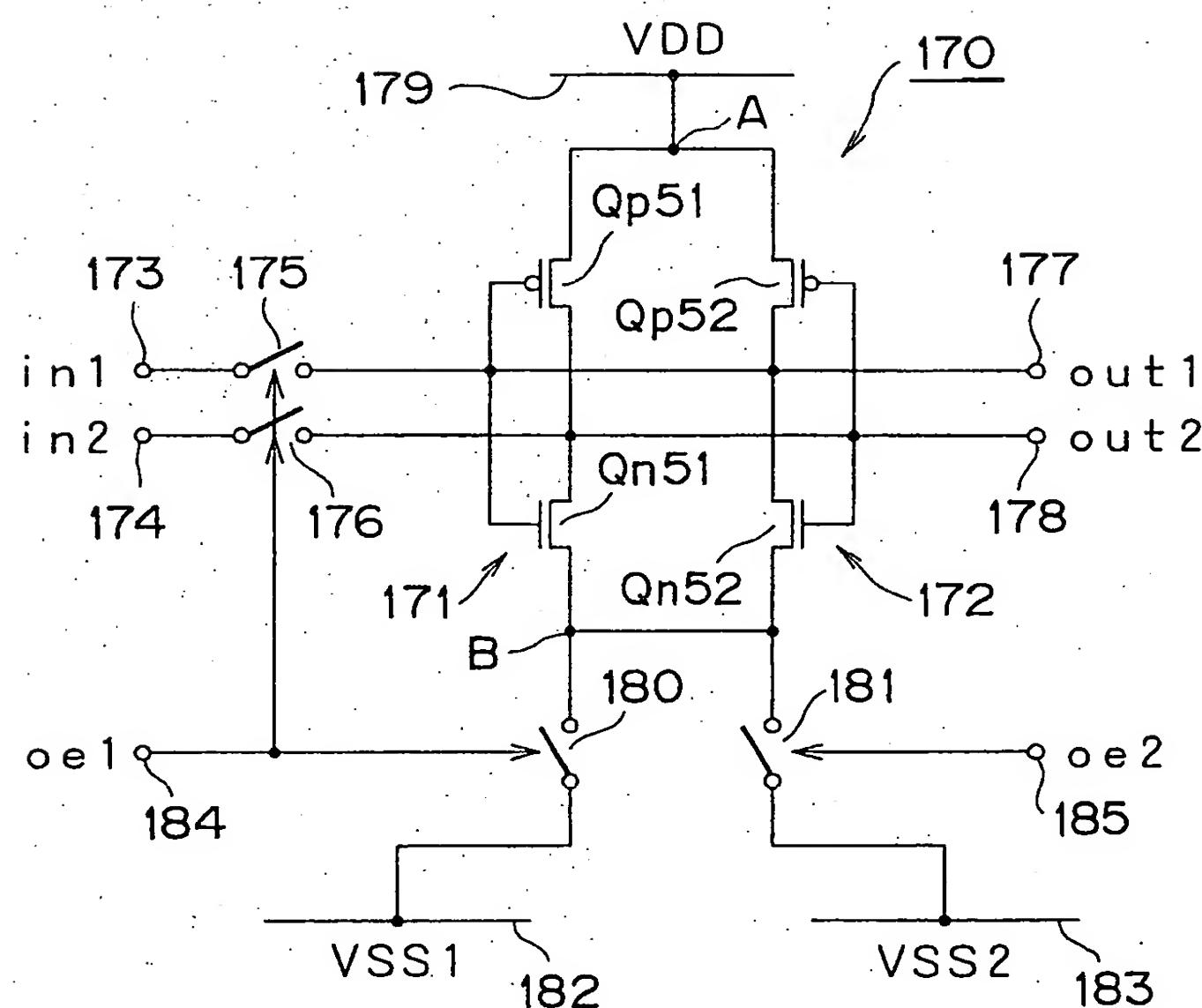
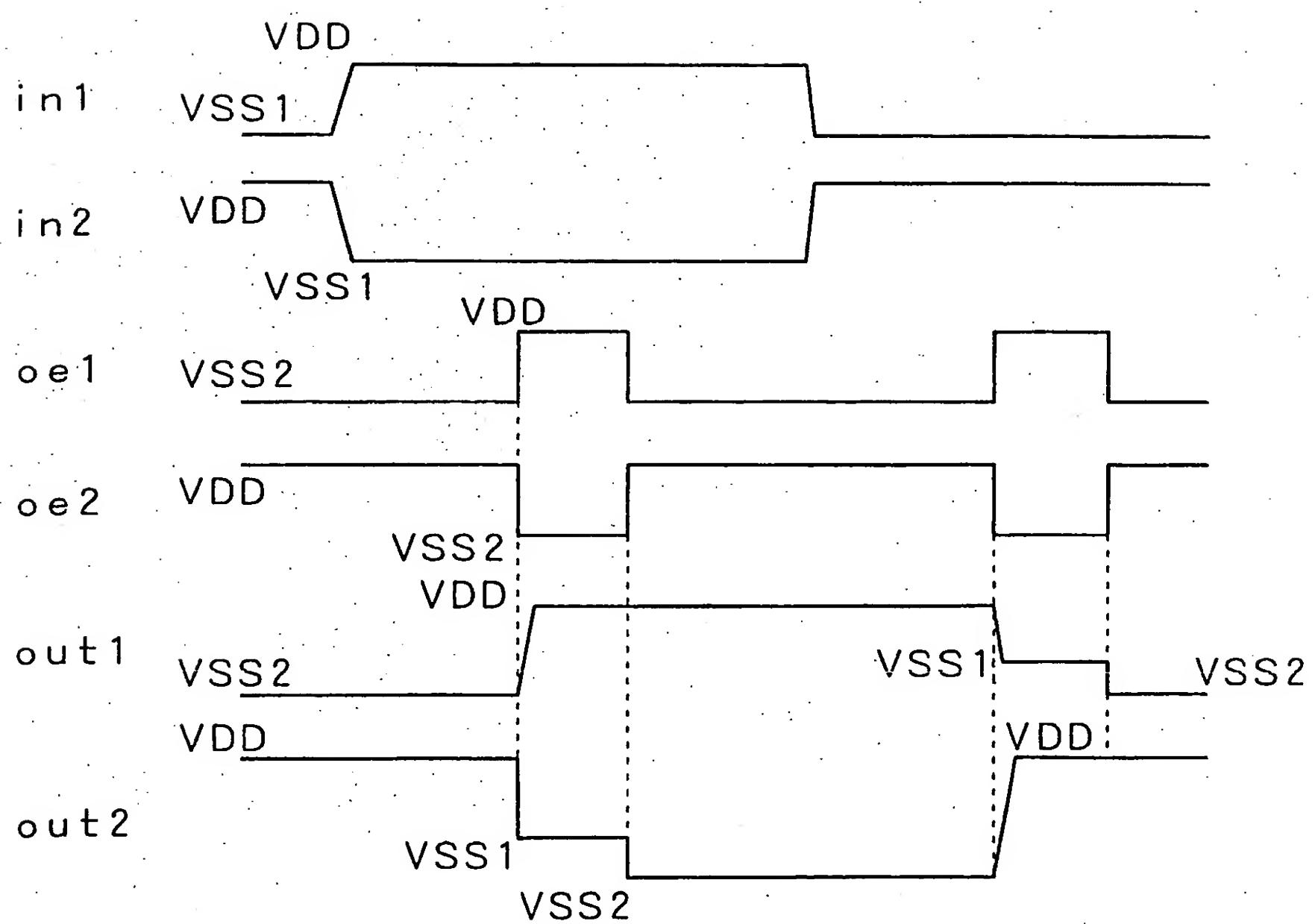


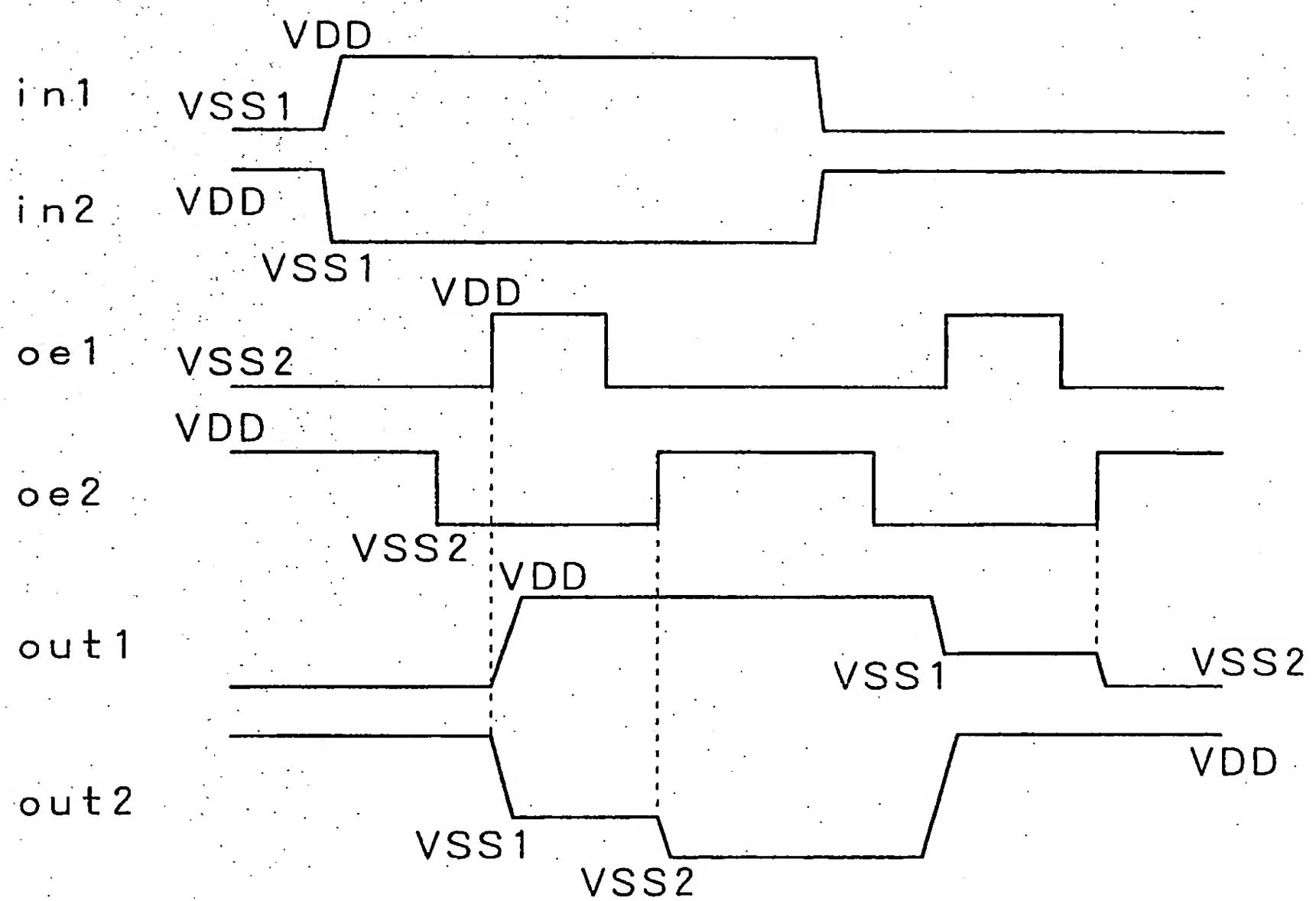
FIG. 26



F I G. 27



F I G. 28



F I G. 29

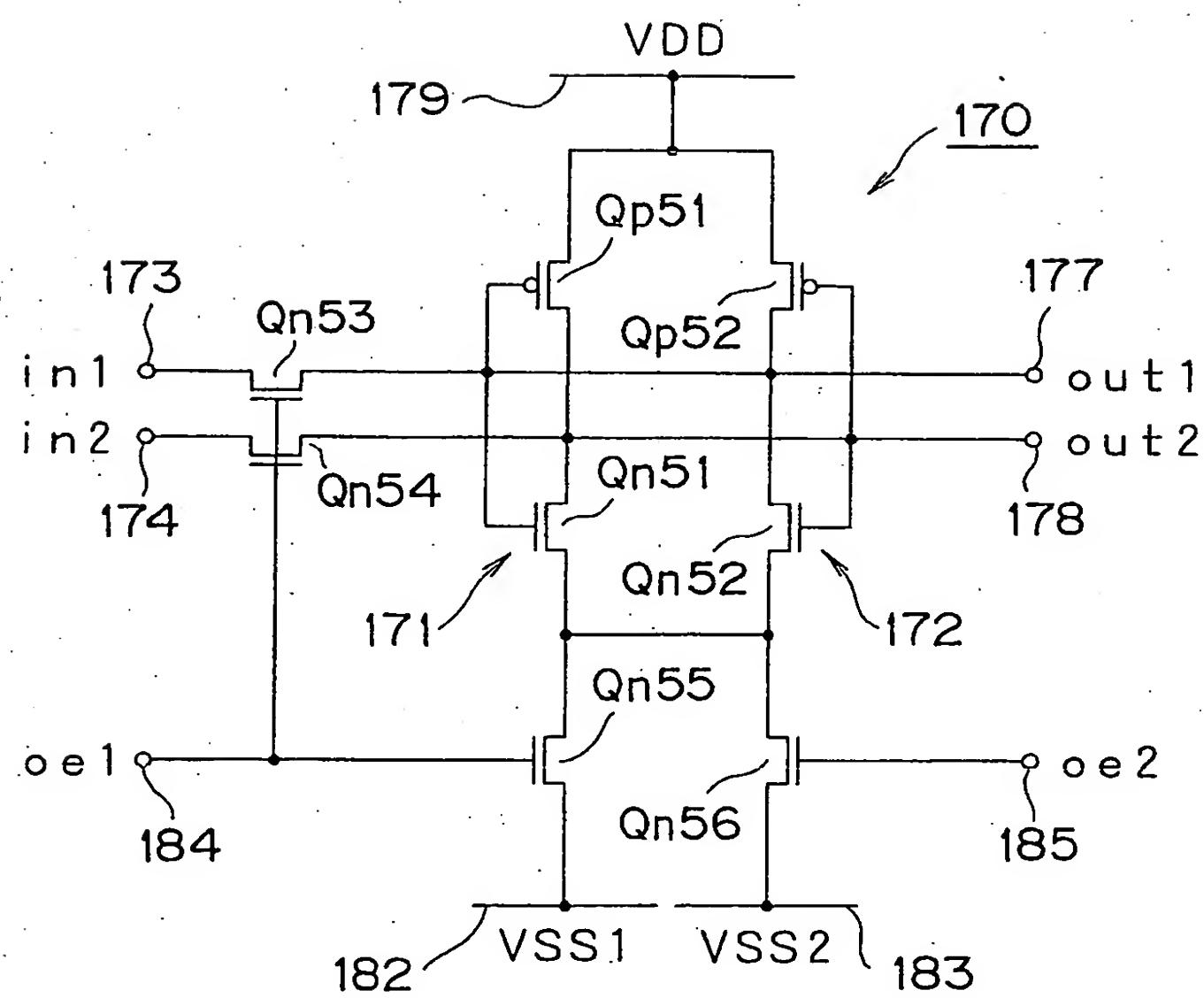


FIG. 30

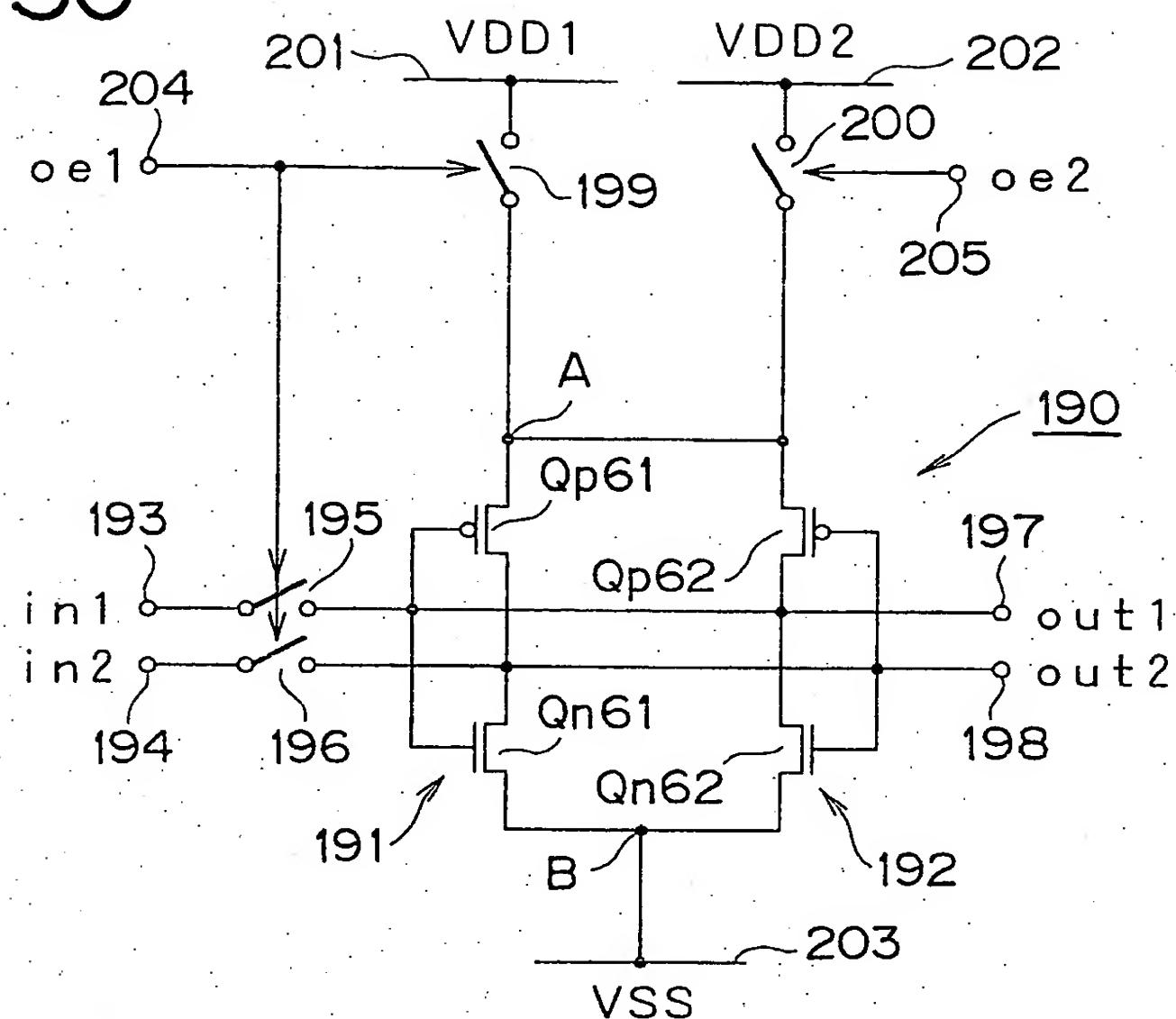
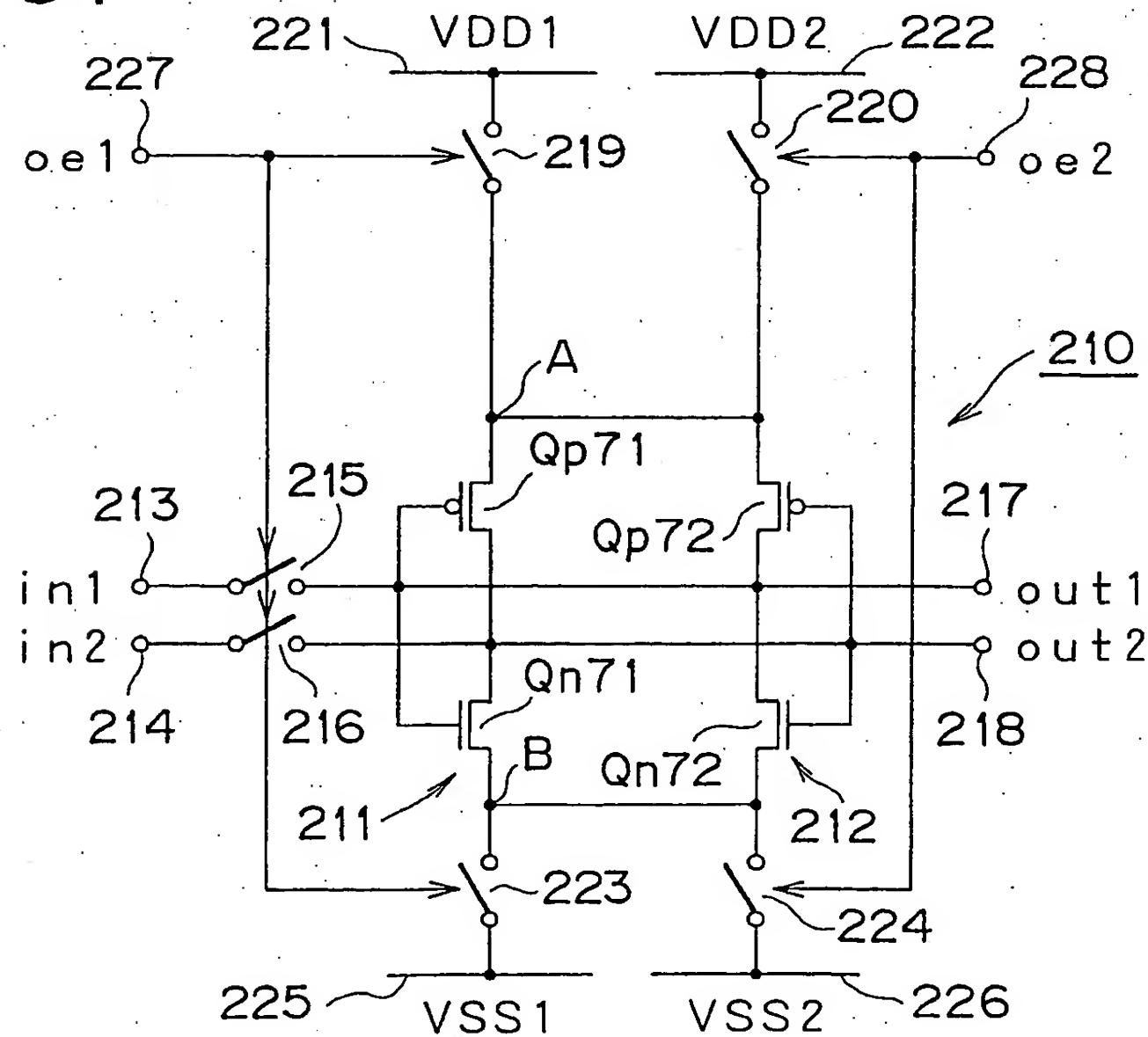


FIG. 31



F I G. 32

SAMPLING PULSE
(FROM HORIZONTAL
SHIFT REGISTER)

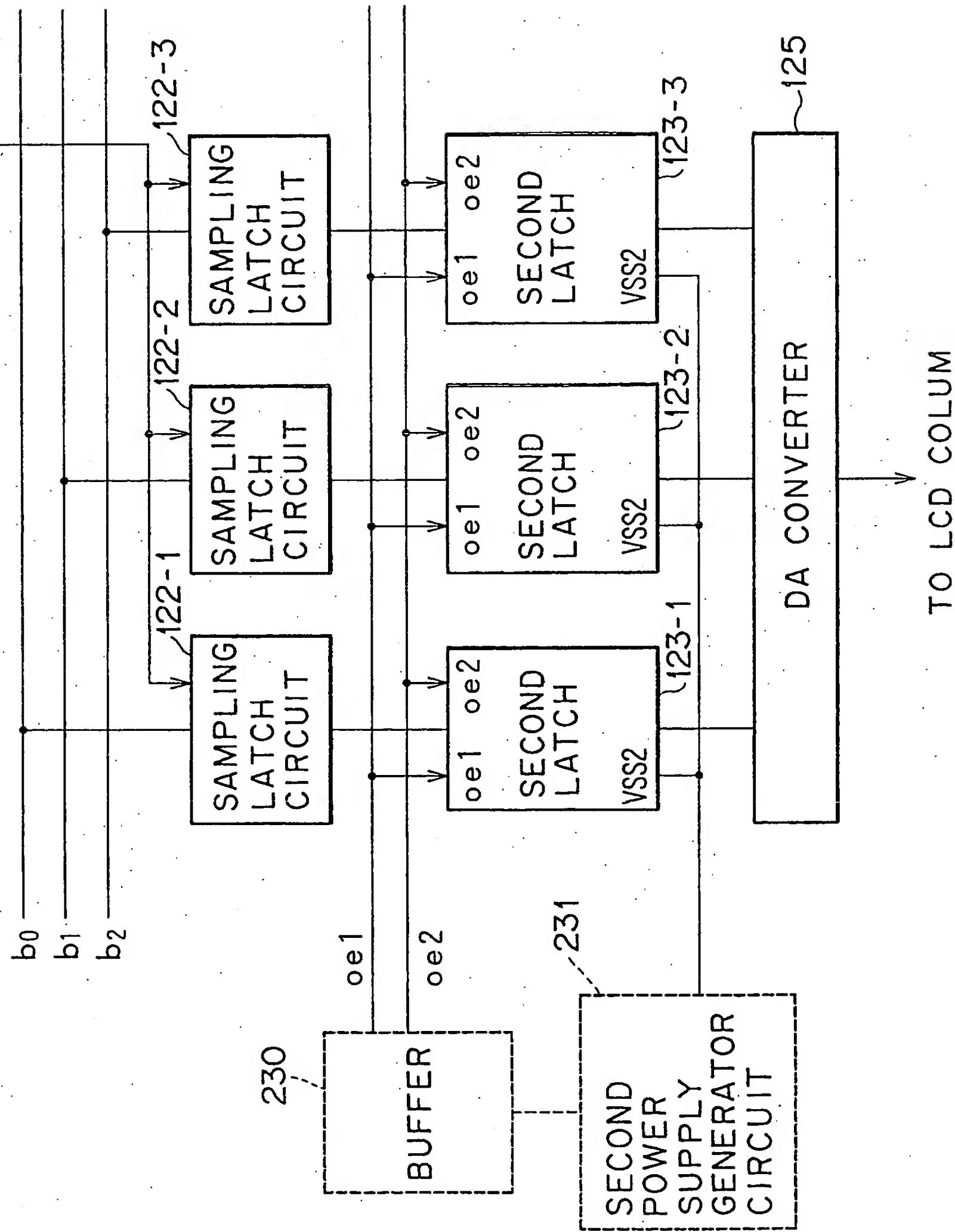


FIG. 33

SAMPLING PULSE
(FROM HORIZONTAL
SHIFT REGISTER)

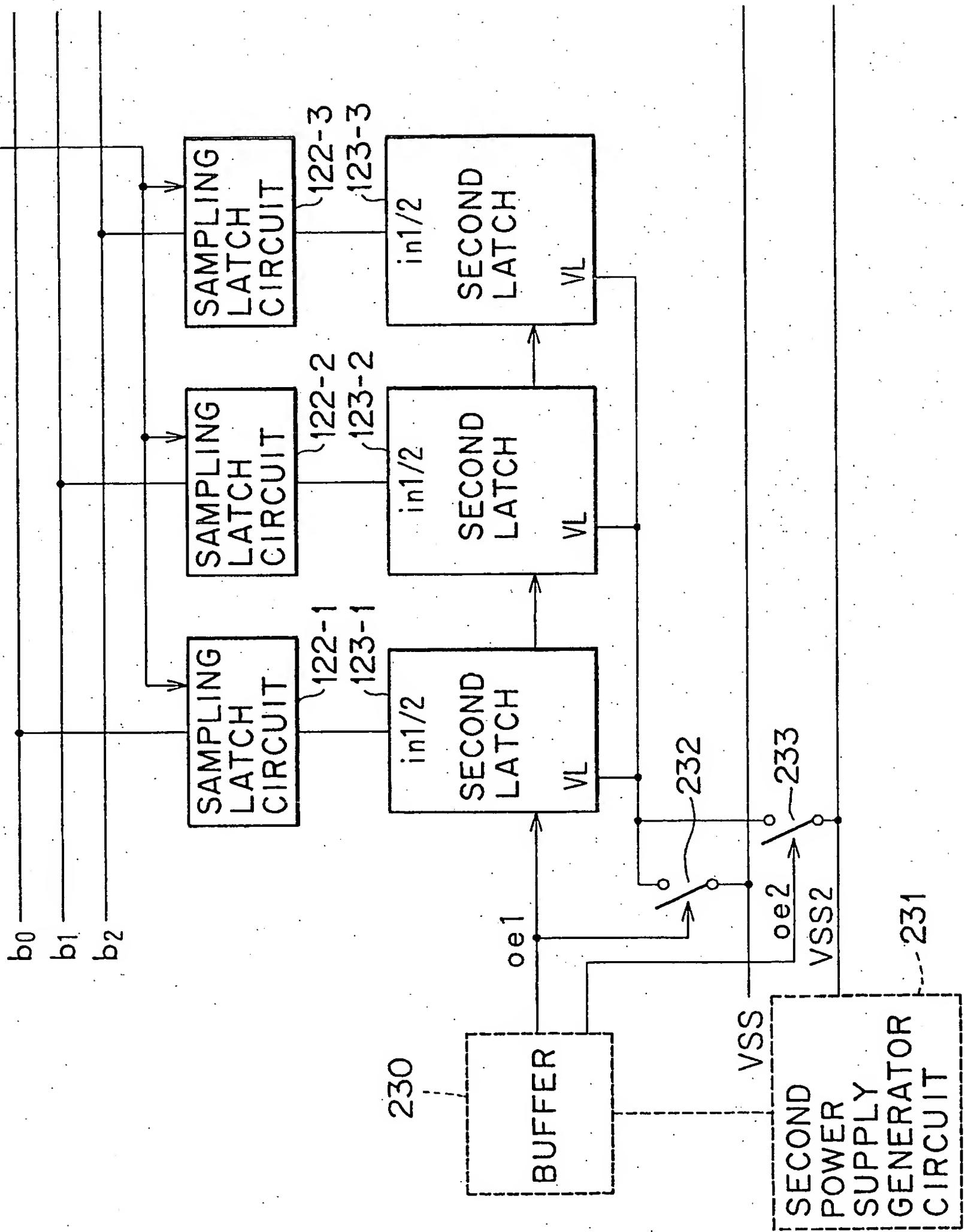


FIG. 34

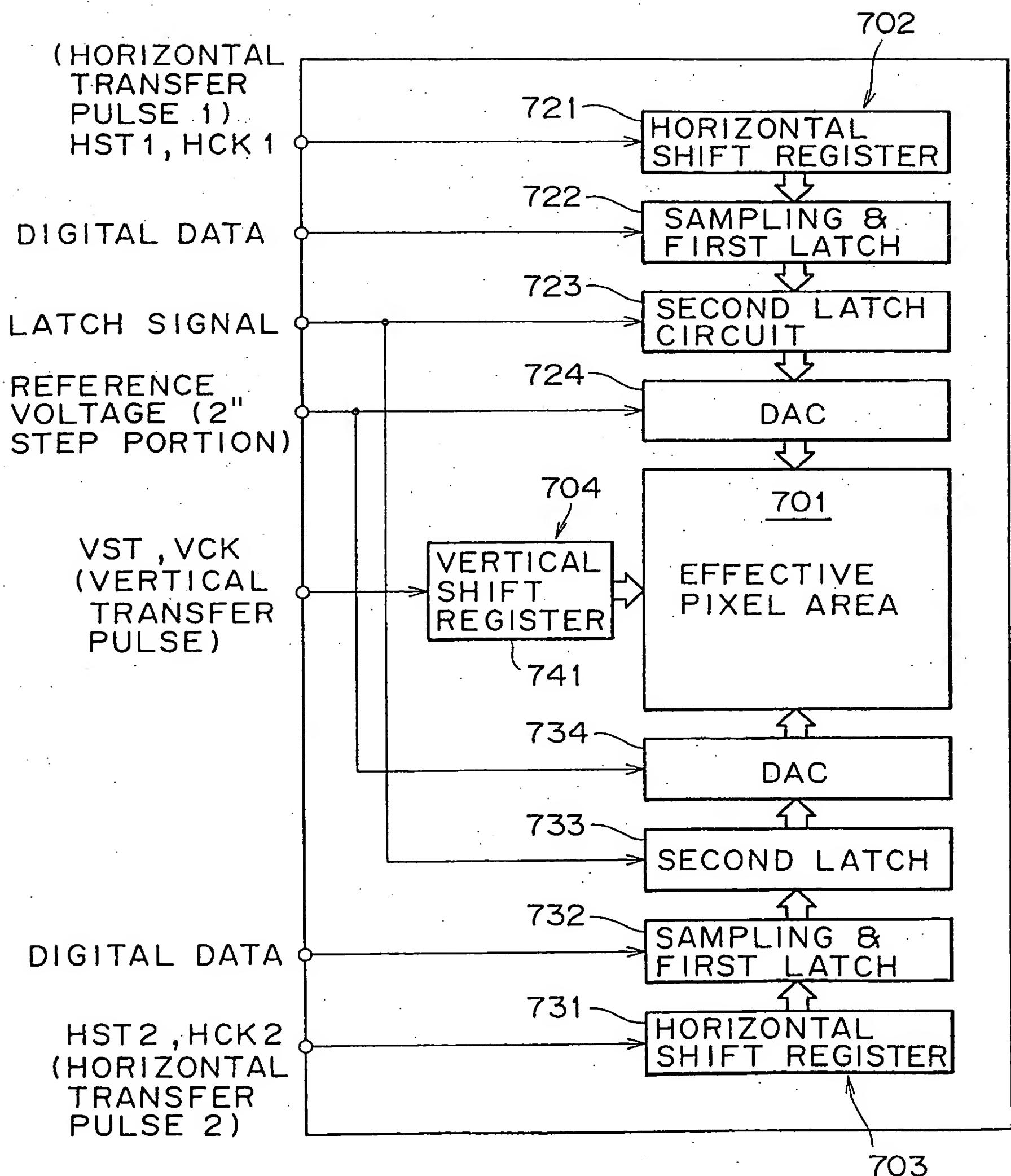


FIG. 35

